REVISION RECORD		
REV.	DESCRIPTION:	DATE:
1.0	ECN 121-2016 Initial Release	08/2/16
2.0	ECN 149-2016	08/22/16
3.0	ECN 19-2017	02/15/17

SOURCE
VIN_EXT
J5
AMP
640456-2
Q4
Fairchild
FDN340P
C6
2.2uF

3V3
J2
WL_HOST_WAKE
EXT_32KHZ_CLK
R21
0
NP
CLK_REQ
WIFI_GPIO_1
WIFI_GPIO_2
WIFI_GPIO_3
BT_GPIO_5
WIFI_GPIO_4
Sullins
PBC08DAAN

Sullins
SPN02SXCN-RC
3V3
JMP2
3.3V_MOD
J6
Sullins
PRPN021PAEN-RC
C1
100nF

VCC_IO
R3
10K
NP
WL_REG_ON
R2
100K
CLK_REQ

MOD1
450-0169
LSR
RF Module, Sterling-LWB5, Chip Antenna
1 GND
2 BT_PCM_SYNC
3 BT_PCM_IN
4 BT_PCM_OUT
5 VBAT
6 GND
7 RF_SW_CTRL_9
8 RF_SW_CTRL_8
9 GPIO_3
10 GPIO_5
11 GPIO_4
12 WL_REG_ON
13 GPIO_0/WL
14 GND
15 NC
16 NC
17 NC
18 GND
19 LPO_IN
20 VDDIO
21 BT_REG_ON
22 SDIO_DATA_0
23 SDIO_DATA_1
24 GND
25 SDIO_DATA_2
26 SDIO_CMD
27 SDIO_DATA_3
28 GND
29 SDIO_CLK
30 GND
31 BT_UART_RTS_HST_CTS
32 BT_UART_CTS_HST_RTS
33 BT_UART_TXD_HST_RXD
34 BT_UART_RXD_HST_TXD
35 BT_I2S_CLK
36 BT_I2S_DO
37 BT_I2S_WS
38 BT_PCM_CLK
39 BT_DEV_WAKE
40 BT_HST_WAKE
41 GND
42 GND
43 GND
44 GND
45 GND
46 GND
47 GND

BT_GPIO_3
BT_GPIO_4
BT_GPIO_5

VCC_IO
R20 10K NP
R18 10K NP
R15 10K NP
R16 10K NP
R17 10K NP
R19 10K NP
SDIO_CLK
SDIO_DAT3
SDIO_CMD
SDIO_DAT2
SDIO_DAT1
SDIO_DAT0
NP
10K
R1

VIN_EXT
VCC_IO_EXT
J3
BT_HST_WAKE
BT_PCM_SYNC
BT_PCM_IN
BT_PCM_OUT
WL_REG_ON
BT_REG_ON
BT_DEV_WAKE
BT_PCM_CLK
BT_UART_CTS_HST_RTS
BT_UART_TXD_HST_RXD
BT_UART_RXD_HST_TXD
BT_UART_RTS_HST_CTS
Sullins
PBC08DAAN

VIN_EXT
3V3
3.3V_SDIO
J7
JMP1
QPC02SXGN-RC
Sullins
Sullins
PBC03SAAN

VCC_IO
3.3V_MOD
R11
0
NP
R12
0
Y1
VCC
OUT
EN
GND
Abracon
ASDK-32.768KHZ-LR
C7
1000pF
R13
0
EXT_32KHZ_CLK

VCC_IO
R5
10K
NP
BT_REG_ON

VCC_IO
VCC_IO_EXT
R14
10K
NP
C2
100nF
3V3
J4
JMP3
QPC02SXGN-RC
Sullins
PBC03SAAN

SOURCE
3.3V_SDIO
CFP1
SDIO_DAT3
SDIO_CMD
SDIO_CLK
SDIO_DAT0
SDIO_DAT1
SDIO_DAT2
R10 33R
R4 33R
R6 33R
R7 33R
R8 33R
R9 33R
1 CD_DAT3
2 CMD
3 VSS1
4 VDD
5 CLK
6 VSS2
7 DAT0
8 DAT1
9 DAT2
NP

PCB1
LSR
750-00960
PCB, Sterling-LWB and LWB5 SD Card

J8
Harwin
S1751-46R

LSR
a Laird Business
W66 N220 COMMERCE COURT
CEDARBURG, WI 53012
PHONE: (262) 375-4400

DRAWN BY: PGS	DATE: 07/21/16	TITLE: MAIN	
CHECKED BY: CMH	DATE: 07/21/16	PROJECT: Sterling-LWB5 SD Card, Chip Antenna	
HW APPROVED BY: JMB	DATE: 07/21/16	SIZE: C	DRAWING NO: 332-00961 REV: 3.0
FILENAME: 332-00961-R3.0.sch		SCALE: DO NOT	SHEET: 1 OF 1