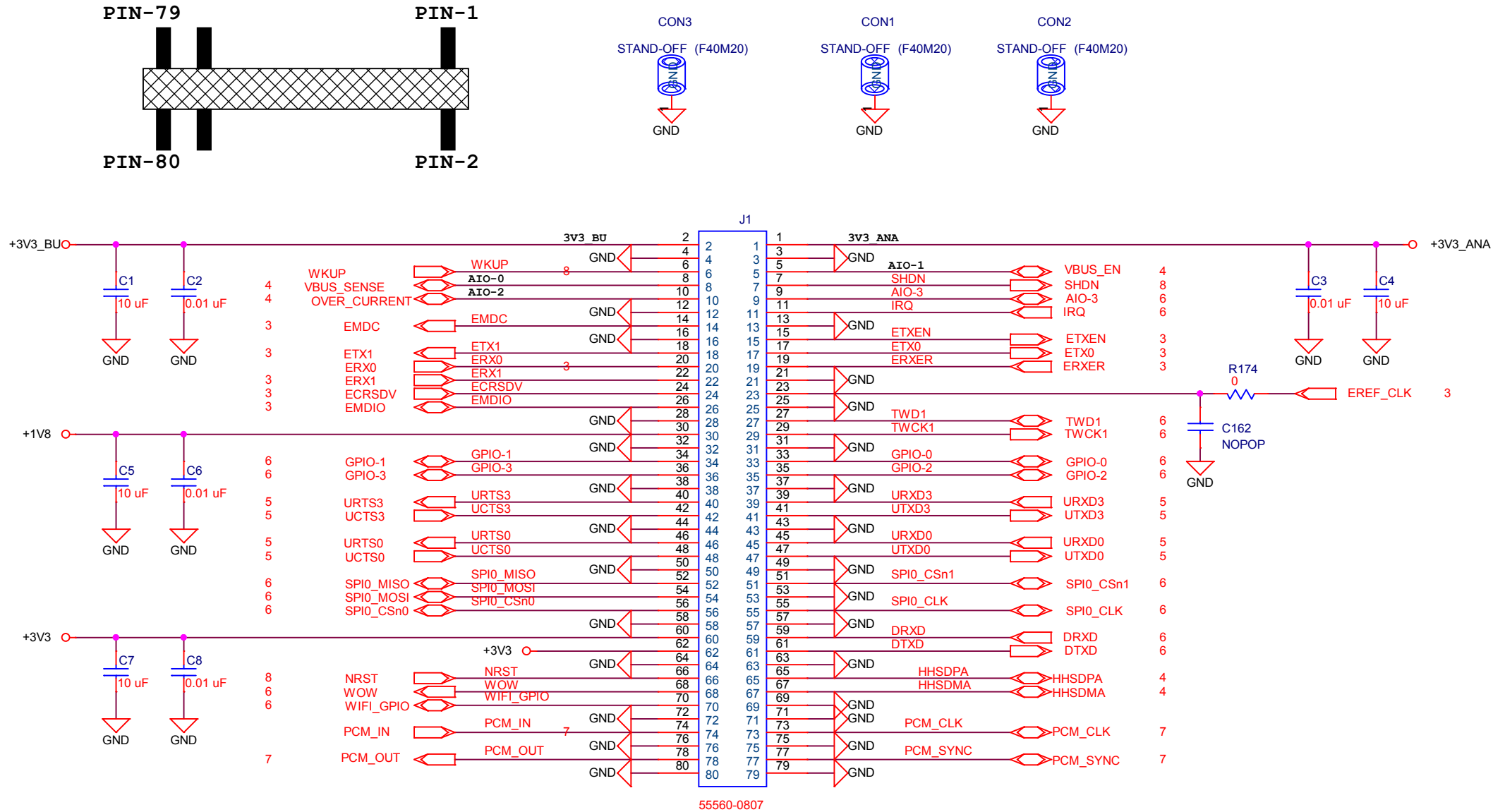


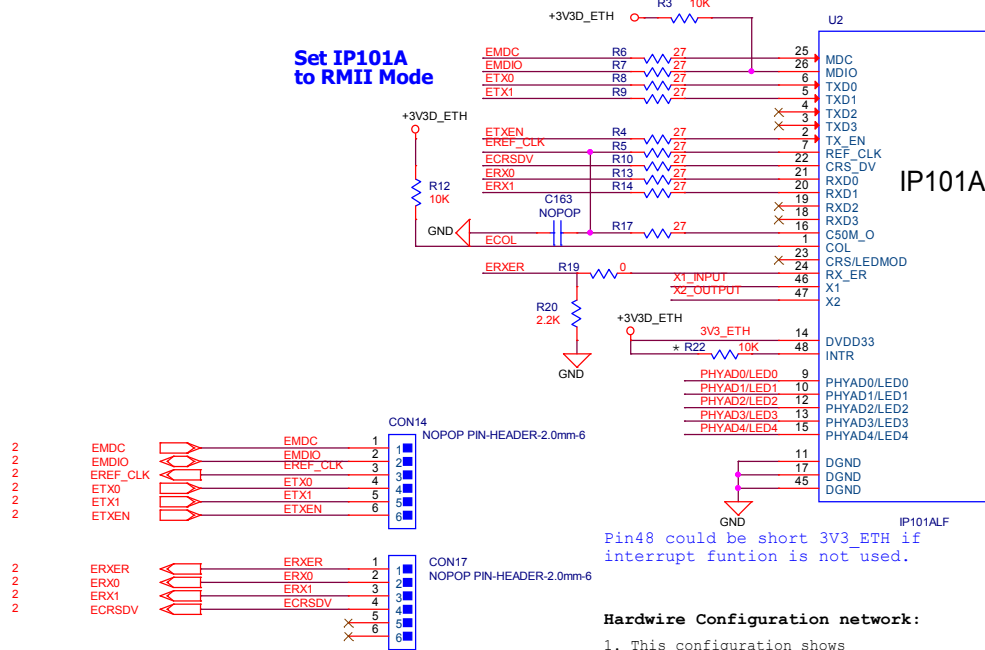
BB PCB PIN OUT (TOP VIEW)



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Hsinchu County 30251, Taiwan (R.O.C.)

Project Name :	Schematic Name :	Drawing By :
BB45NBT	B2B Connector	Jacky Kuo
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Set IP101A to RMII Mode



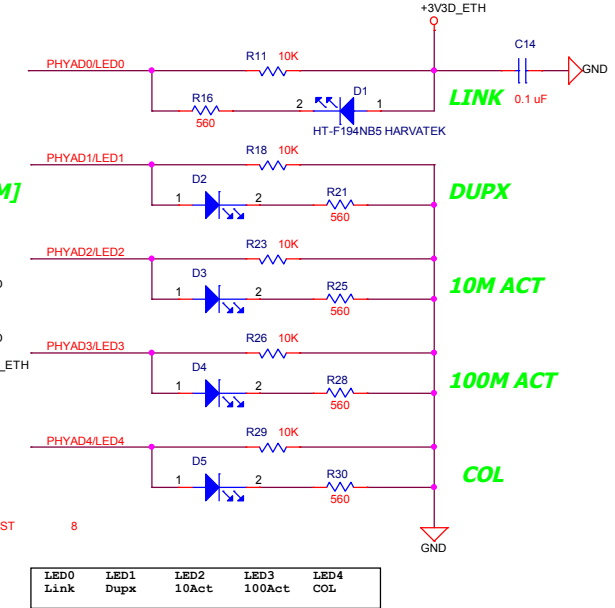
Pin48 could be short 3V3_ETH if interrupt funtion is not used.

Hardwire Configuration network:

- This configuration shows Enable: Auto negotiation, Full duplex, 100Mbps, Link Down Power Saving, RMII interface Disable: Isolate, Repeater mode
- These seven configuration pins could be connected to VDD or GND directly.

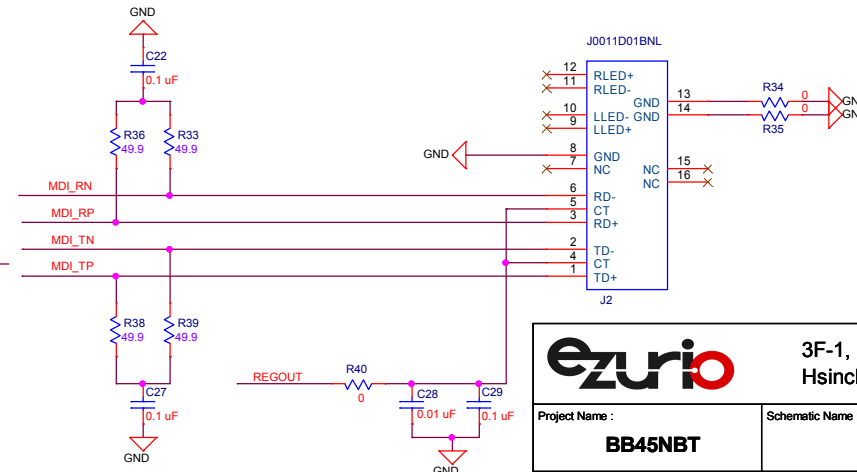
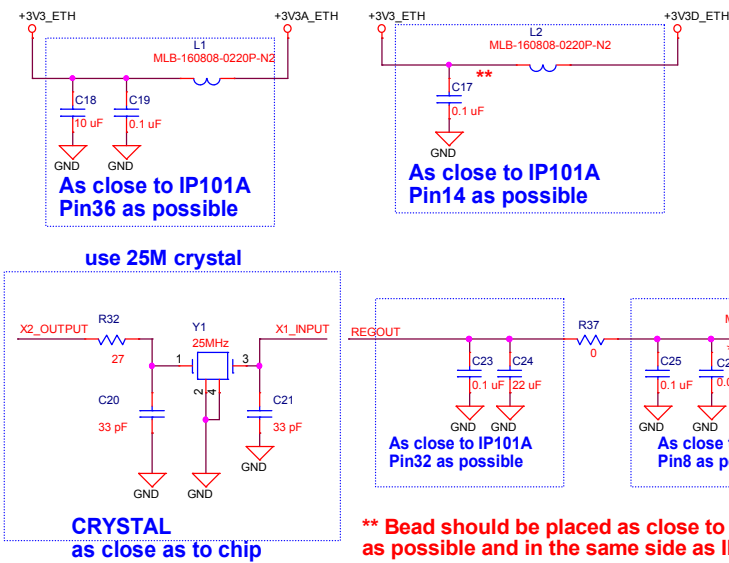
LED and PHY address Configuration

This schematic sets PHY address to 00001



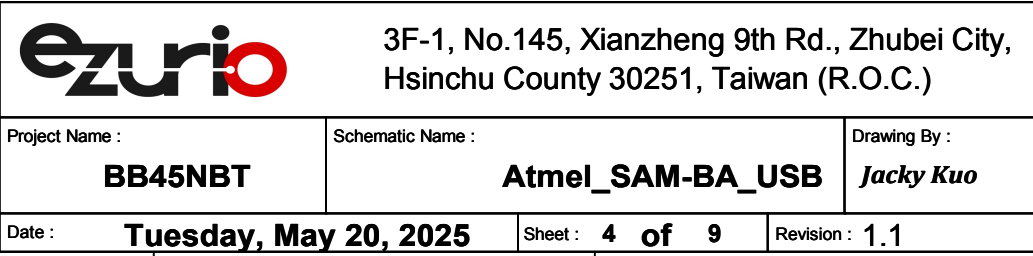
* Pull down U2 pin4 for RMII clock source 25MHz.

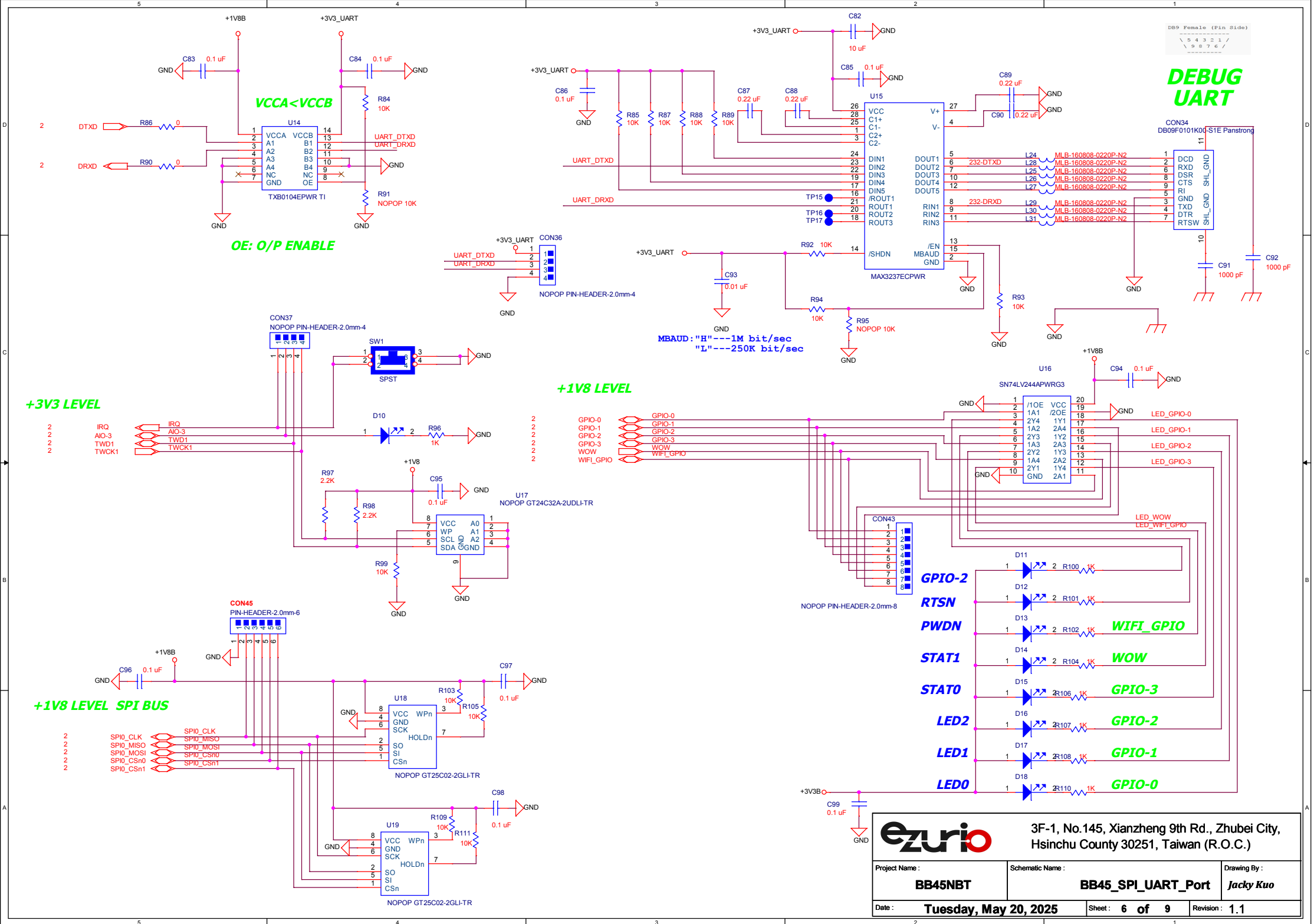
Pin1 COL/RMII	Pin44 MII/SNIB	Function
1	1	RMII, ext 50MHz osc clk in to pin7
1	0	RMII, 25MHz crystal or osc from X1,X2; 50MHz clk out to pin16. (Please refer to the following figure for our recommended application circuit.)
0	1	MII, 25MHz crystal or osc from X1,X2
0	0	SNI, 25MHz crystal or osc from X1,X2

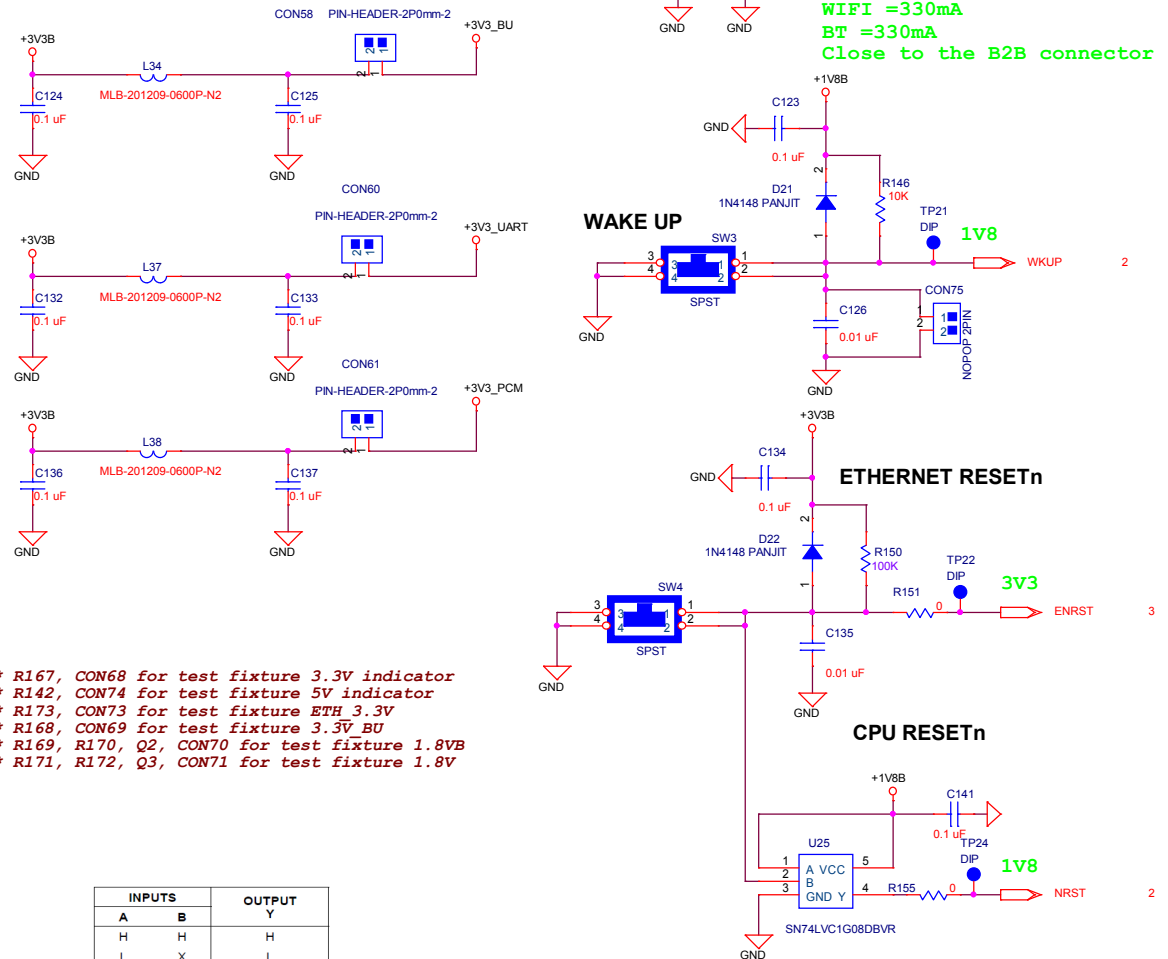
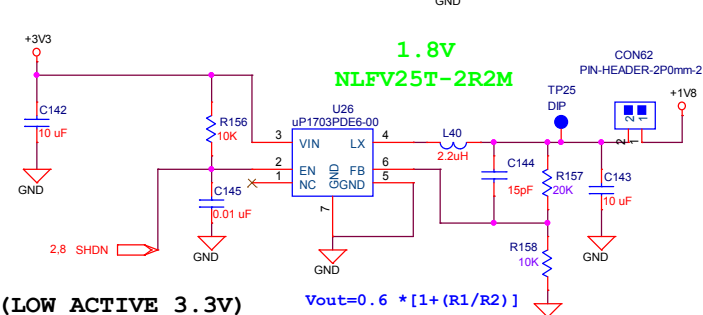


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Project Name : BB45NBT	Schematic Name : SinglePHY Ethernet	Drawing By : Jacky Kuo
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INPUTS		OUTPUT Y
A	B	
H	H	H
L	X	L
X	L	L

LOGIC DIAGRAM (POSITIVE LOGIC)

