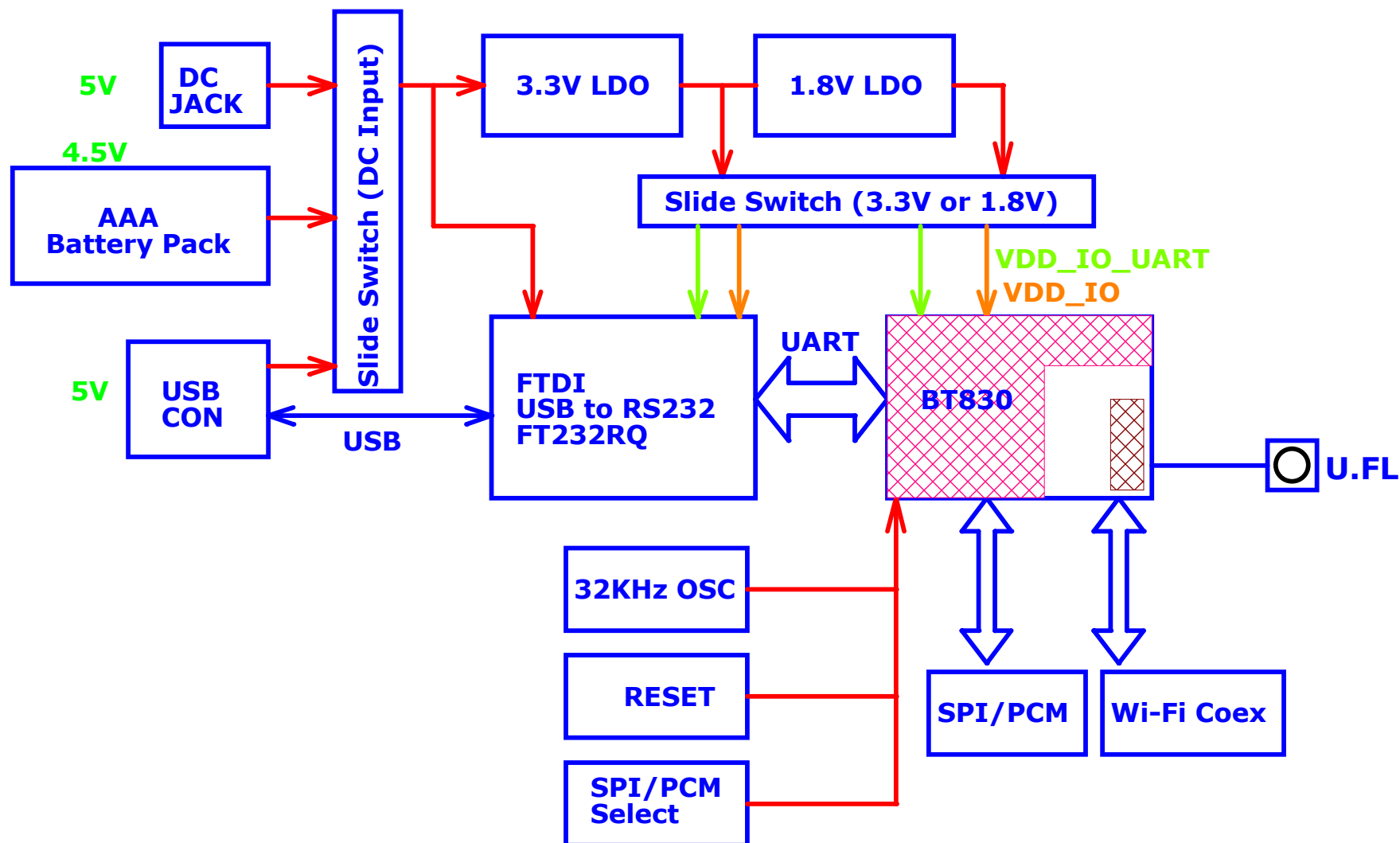




DATE	REVISION NUMBER	INITIALS	Initial Release
2014/02/11	A0 (EVT V00)	Andrew Chen	Initial draft
2014/04/16	B0 (DVT)	Andrew Chen	Add C35, R65,R66
2014/05/29	V01(MP)	Andrew Chen	MP RELEASE
2016/07/15	V1.1	Andrew Chen	correct the reset buttom issue.
2025/4/21	V1.2	AL	Replace brand to Ezurio

PCB design specification

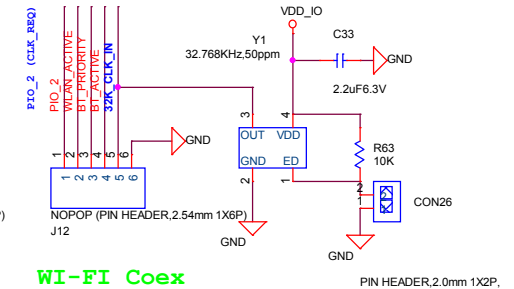
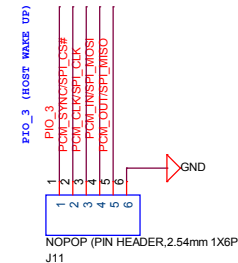
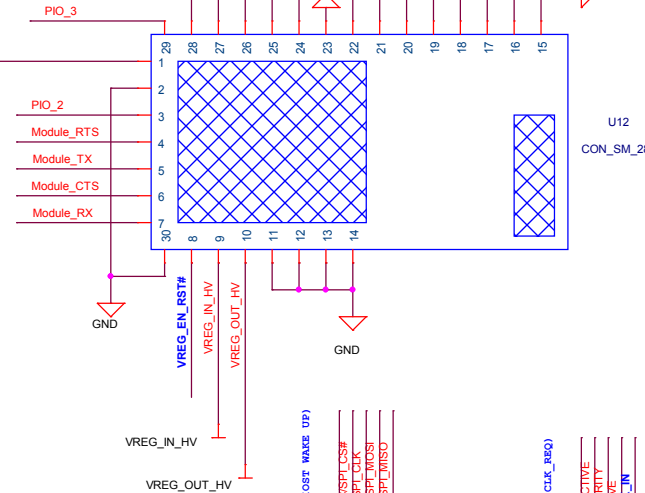
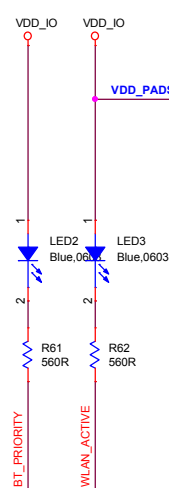
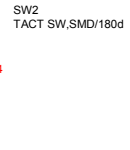
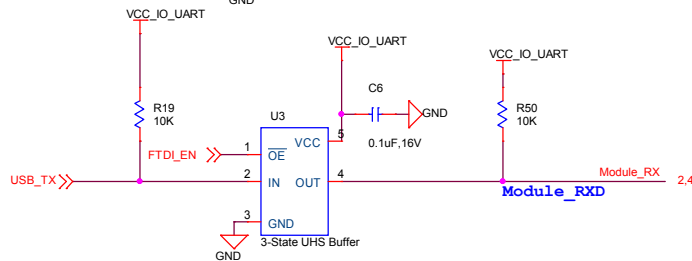
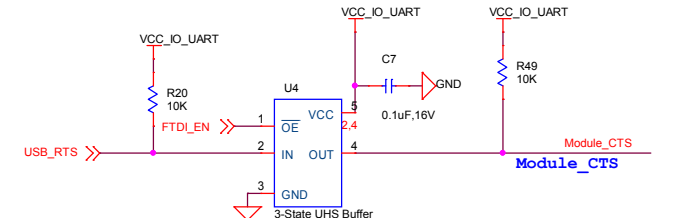
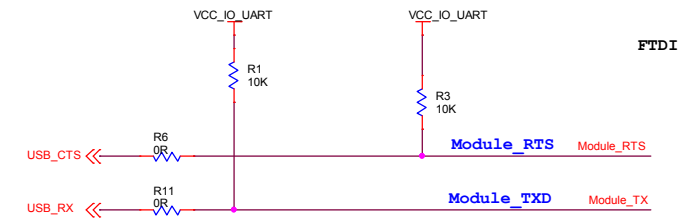
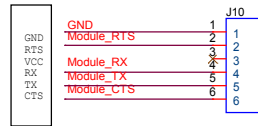
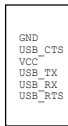
- 1. Substrate: FR4 ROHS compliant, High TG 140 degree.
- 2. Solder mask, color=BLUE, Silkscreen color=WHITE
- 3. Surface finish to be Immersion Nickel/Gold (ENIG) with 1-2 u"
- 4. Start with 1/2 oz. copper on all layers.



J10 for FTDI (USB to TTL 232 Cable)

FTDI (USB to TTL 232 Cable)

NOPOP (PIN HEADER,2.54mm 1X6P)



SPI

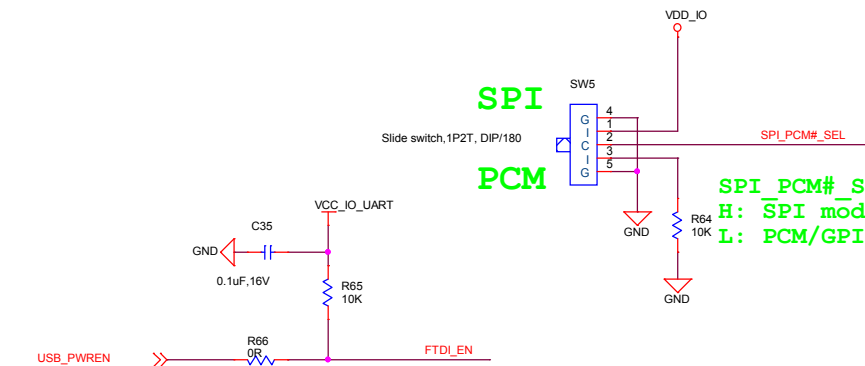
PCM

Slide switch, 1P2T, DIP/180

SPI_PCM# SEL

H: SPI mode

L: PCM/GPIO



ON:
turn off 32KHz.

