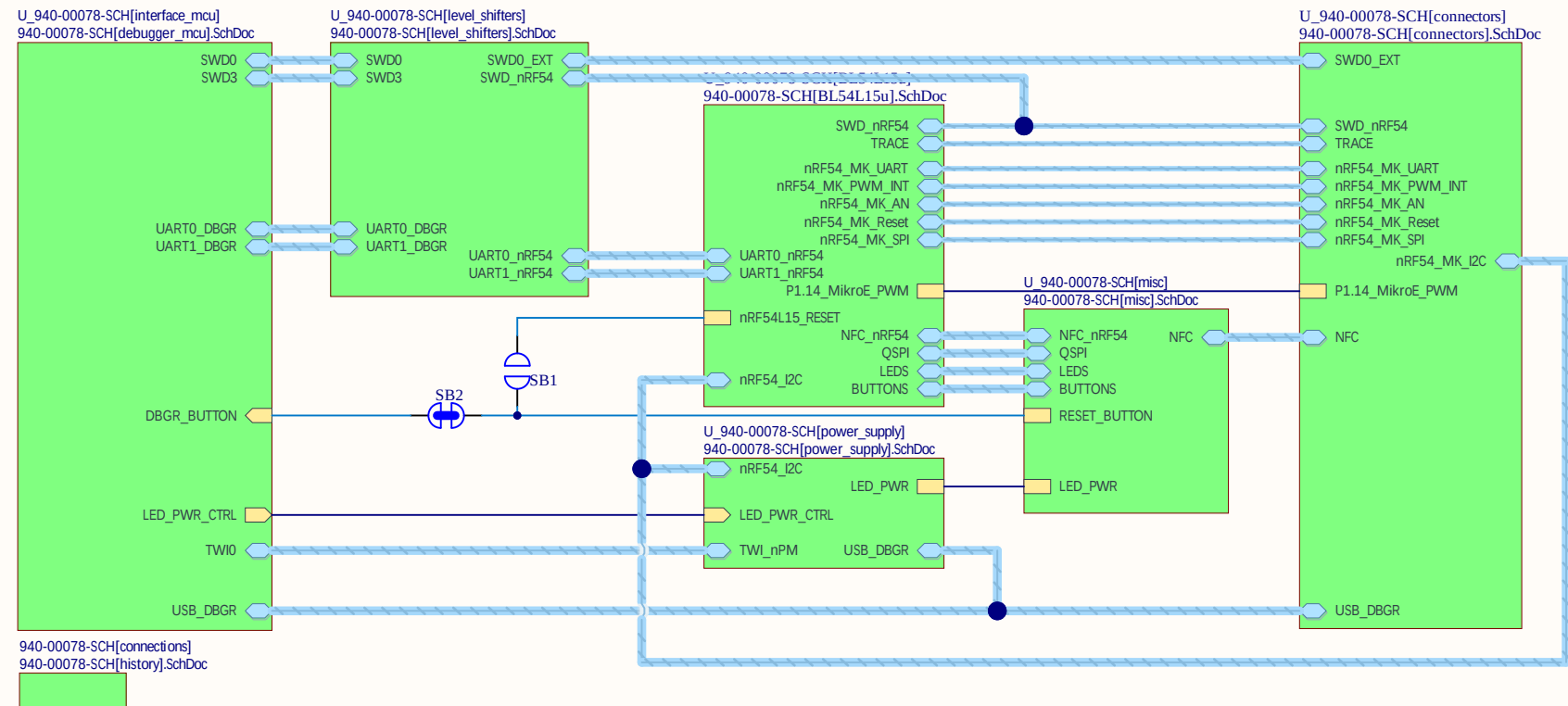


BL54L15u devboard

Sheet 1:	Connections
Sheet 2:	History
Sheet 3:	BL54L15u
Sheet 4:	Debugger MCU
Sheet 5:	Level Shifters
Sheet 6:	Misc
Sheet 7:	Power Supply
Sheet 8:	Connectors



- ✗ The No ERC object is a design directive. This directive is placed on a node in the circuit to suppress harmless warnings and/or error violation conditions that are detected when the schematic project is compiled.

Board fiducials



PCB1

Ezurio, 750-00032

Approvals		Date	 3F-1, No.145, Xianzheng 9th Rd, Zhubei City, Hsinchu County 30251, Taiwan (R.O.C.) Phone: +886-3-552-9695 www.ezurio.com		
Drawn:	RK, AL	8July2024			
Checked:	RK, AC	25July2024			
Approved:	AC	25July2024			
Title: Schematic, BL54L15u Module Dev Board,, Chip Antenna					
Sheet Name: connections		Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0
File: 940-00078-SCH[connections].SchDoc		Customer: Internal	Last Modified: 6/2/2025 AM 10:32:18	Size: B	Sheet: 1 of 8

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1		2		3		4		5		6		
A												A
B												B
C												C
D												D

DATE	REVISION	INITIALS	DESCRIPTION
8July2024	1.0 (EVT1)	RK, AL	EVT1 SCH (based on BL54L15 devboard DVT1 (rev2.0)).
18Sep2024	2-0 (DVT1)	RK, AL	1. Adjust the definition of module pins 1,3,4,5,7,9,11 2. Re-name P5V0 to 5V0 so U5pin1 LDO receives a voltage. 3. Updated the schematic based on Nordic's new design.
12Dec2024 25Dec2024	2-1 (DVT2)	LC, AL	1. Correct the pin4 and pin5 network names of U14 2. Change C41 to NOPOP 3. Add alternative to U12 (TI/ TCA9406YZPR) 4. Change L1 to 0 ohm (YAGEO/ RC0402FR-070RL)
19Mar2025	2-2	LC , AL	1. Change R41 and R42 to 200k/0402 (YAGEO, RC0402FR-07200KL) 2. Change U14 to TI, TXS0102DCU
29May2025	3-0 (PVT)	LC , AL	Fix mikroE SPI device coexistence issue: 1. Change U11 to TXS0102DCU 2. Change R10, R11, R12, R27 to NOPOP

Approvals		Date	 3F-1, No.145, Xianzheng 9th Rd, Zhubei City, Hsinchu County 30251, Taiwan (R.O.C.) Phone: +886-3-552-9695 www.ezurio.com			
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Title: Schematic, BL54L15u Module Dev Board,, Chip Antenna						
Sheet Name: history		Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0	
File: 940-00078-SCH[history].SchDoc		Customer: Internal	Last Modified: 6/2/2025 AM 10:32:18	Size: B	Sheet: 2 of 8	

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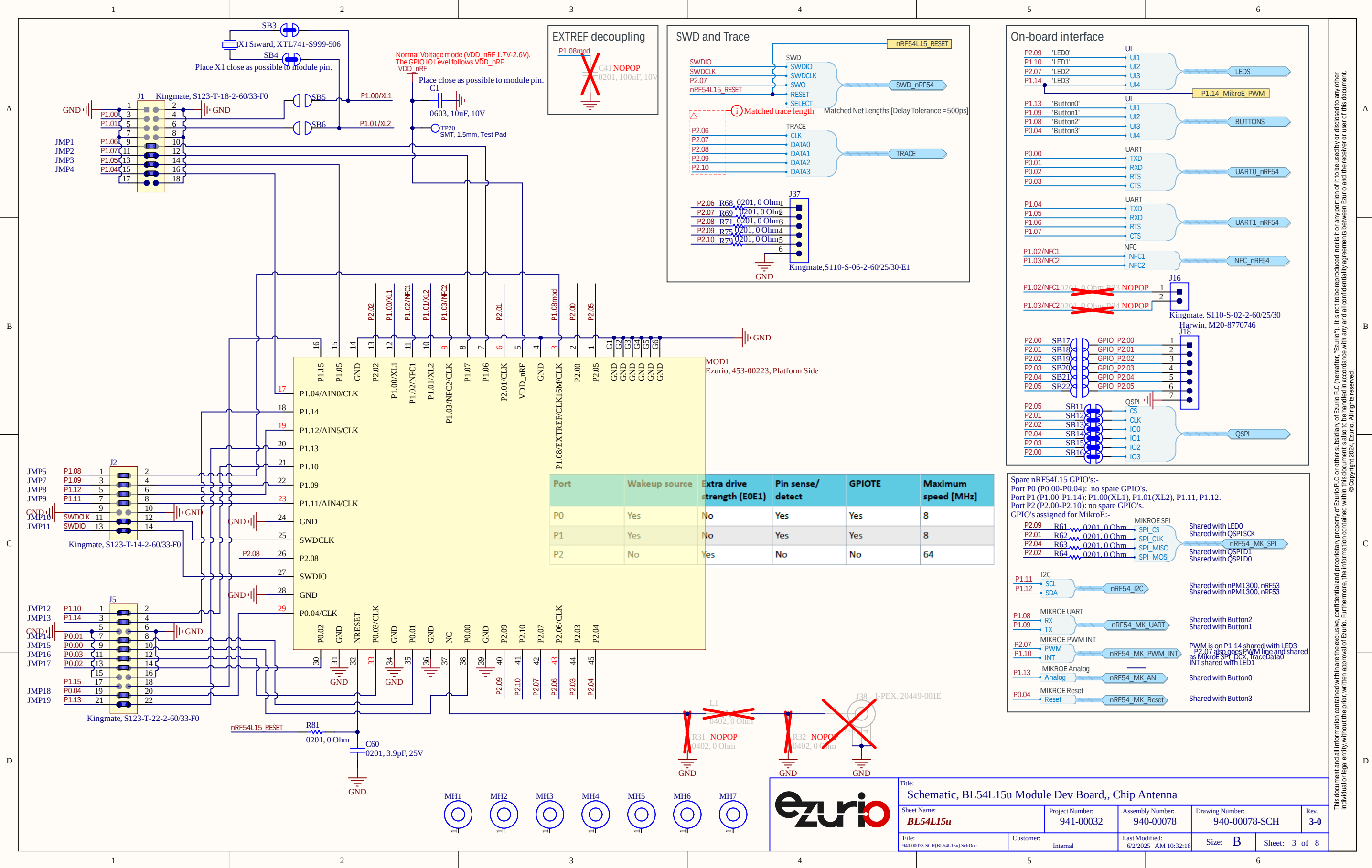
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DATE	REVISION	INITIALS	DESCRIPTION
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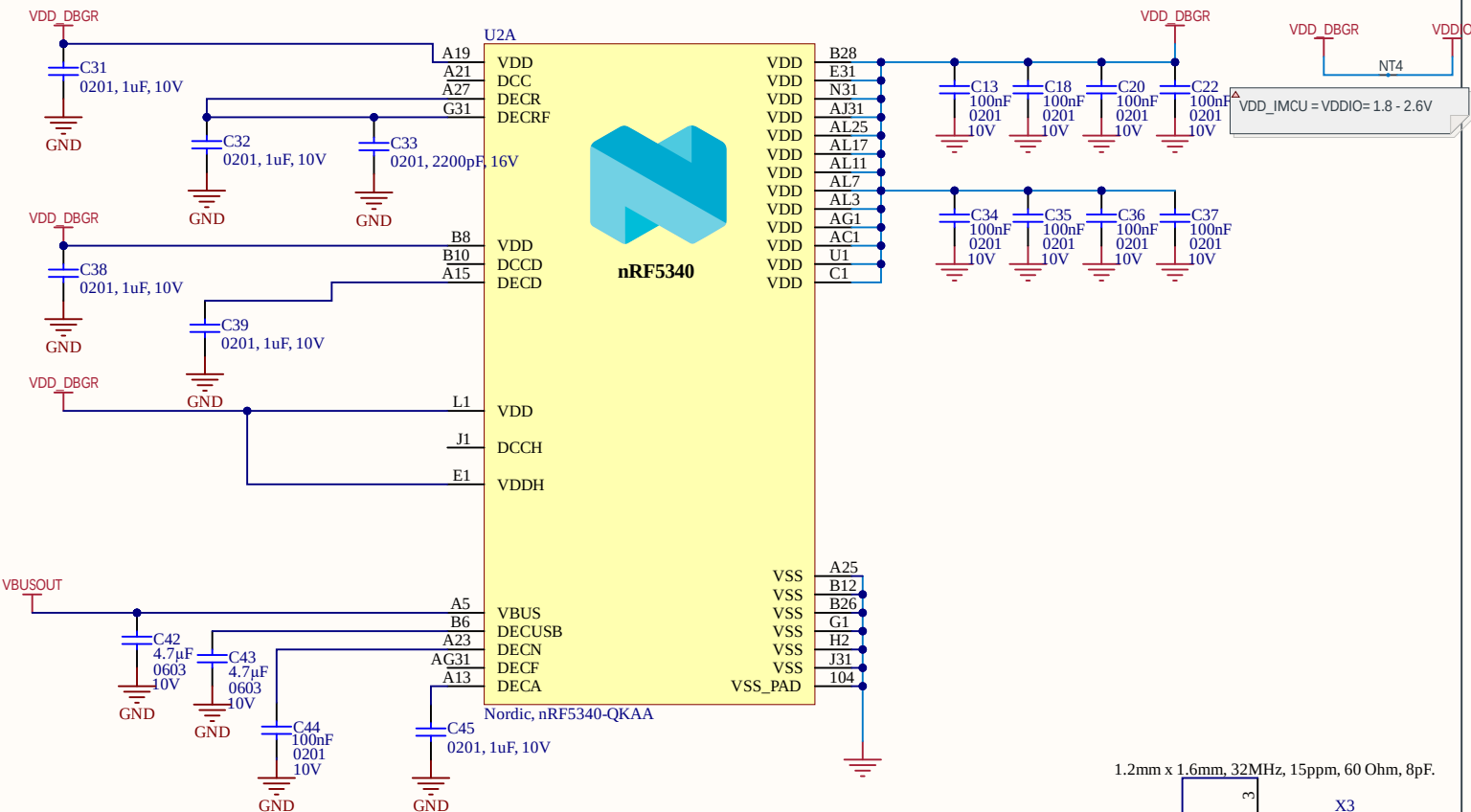
Approvals		Date	 3F-1, No.145, Xianzheng 9th Rd, Zhubei City, Hsinchu County 30251, Taiwan (R.O.C.) Phone: +886-3-552-9695 www.ezurio.com		
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Title: Schematic, BL54L15u Module Dev Board,, Chip Antenna					
Sheet Name: <i>history</i>		Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0
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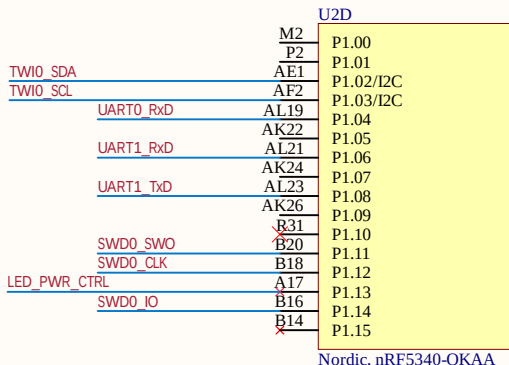
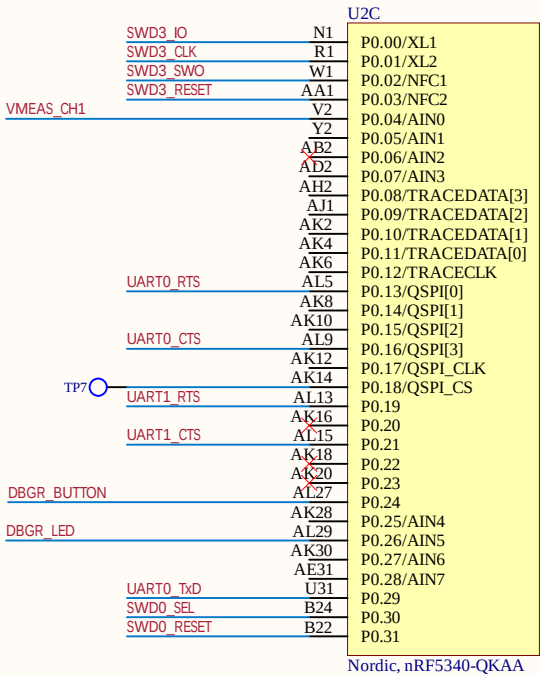
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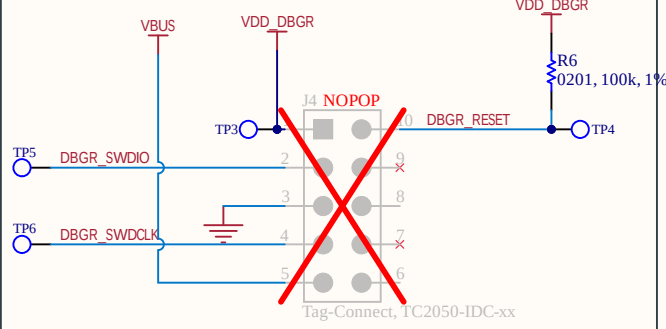
Interface MCU



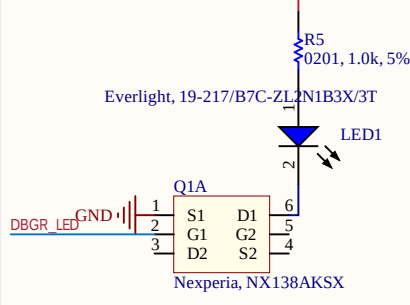
DECF chip pin is No connect on Nordic PDK.



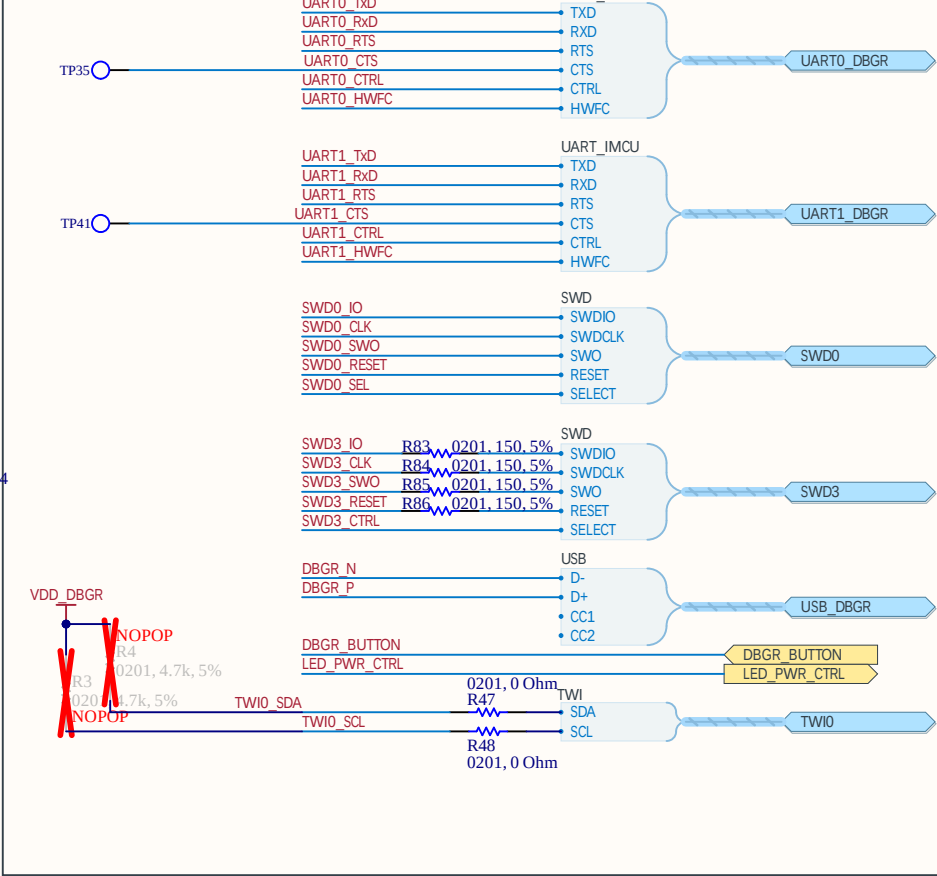
Interface MCU Programming Connector



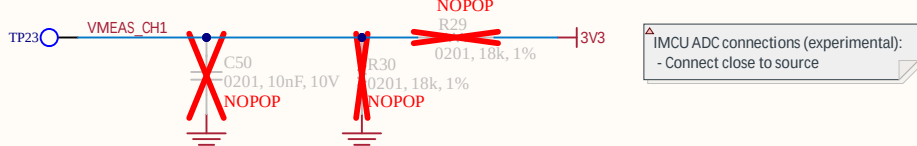
LED



Ports



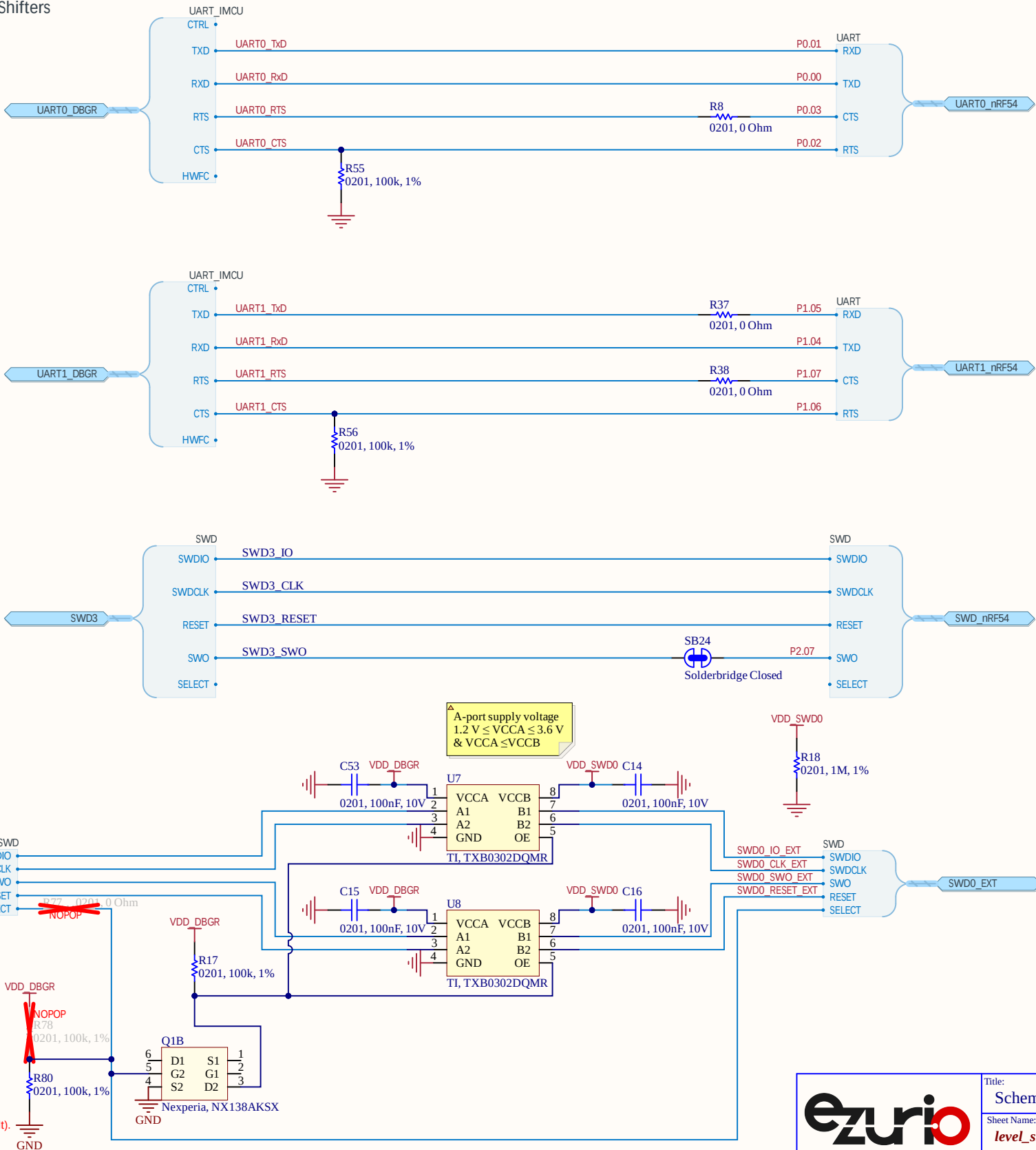
Board TWI



Title: Schematic, BL54L15u Module Dev Board,, Chip Antenna				
Sheet Name: <i>debugger_mcu</i>	Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0
File: 940-00078-SCH[debugger_mcu].SchDoc	Customer: Internal	Last Modified: 6/2/2025 AM 10:32:18	Size: B	Sheet: 4 of 8

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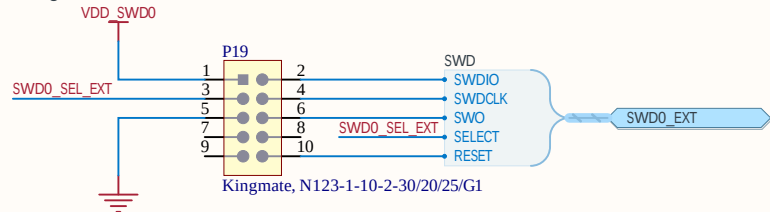
Level Shifters



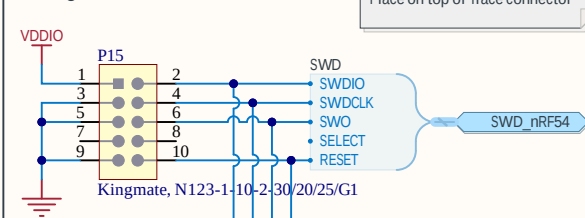
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Sheet Name: level_shifters	Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0
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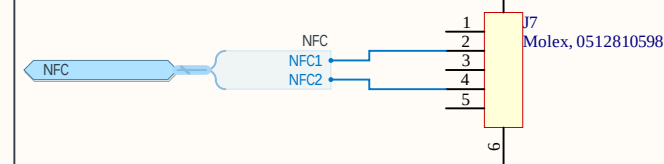
Debug OUT Connector



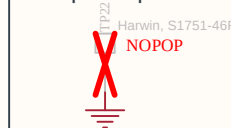
Debug IN Connector



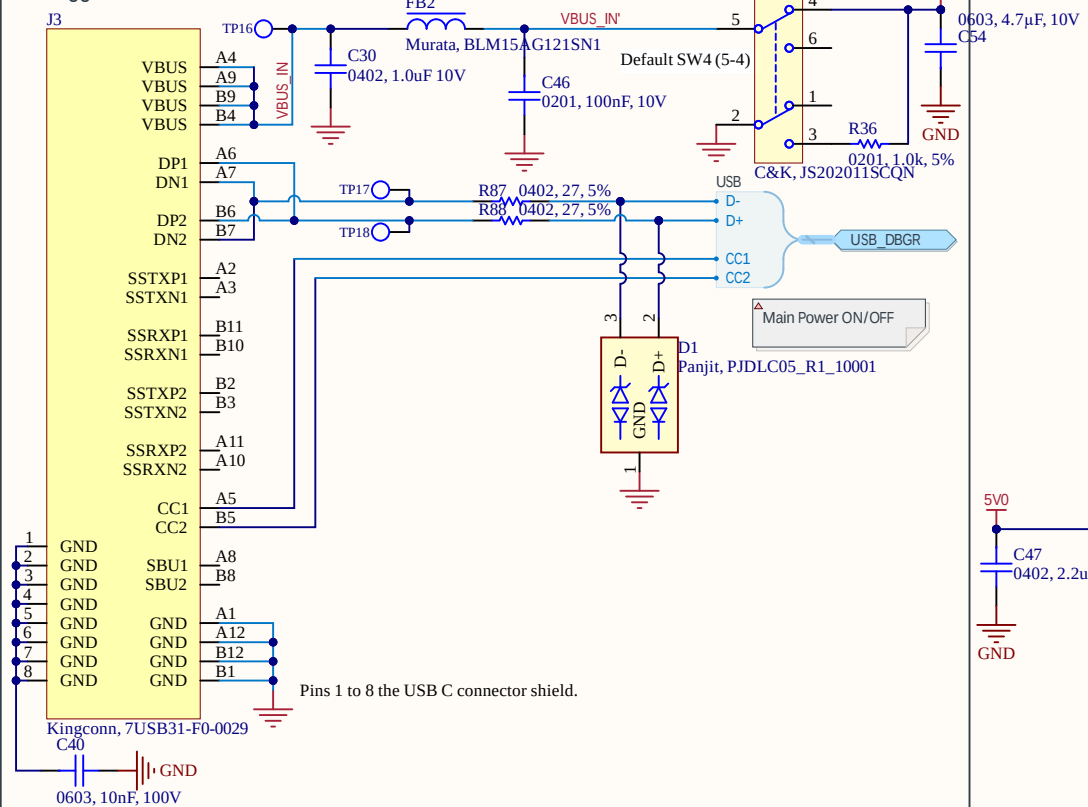
NFCT Antenna Connector



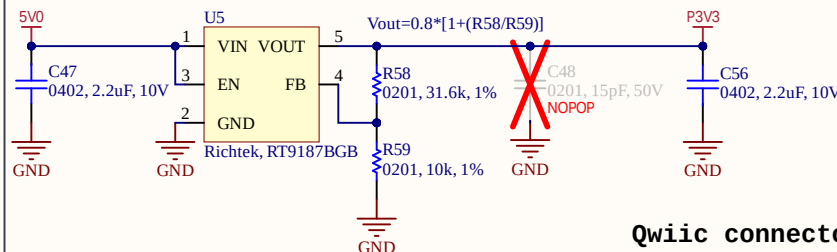
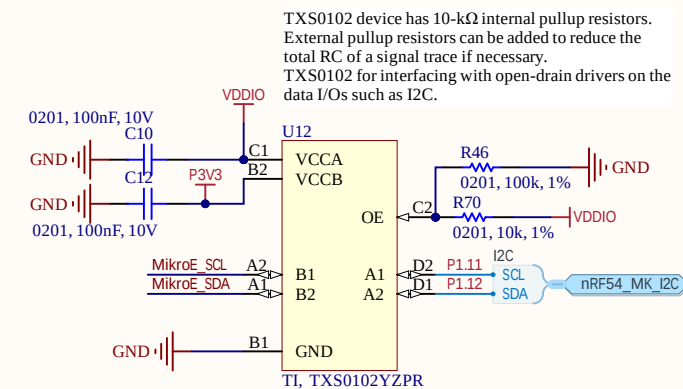
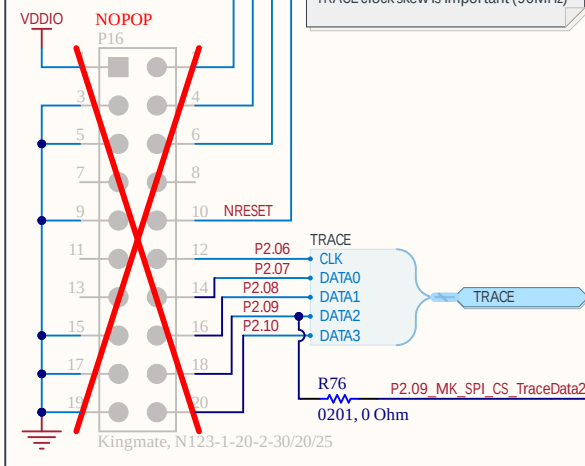
GND probe point



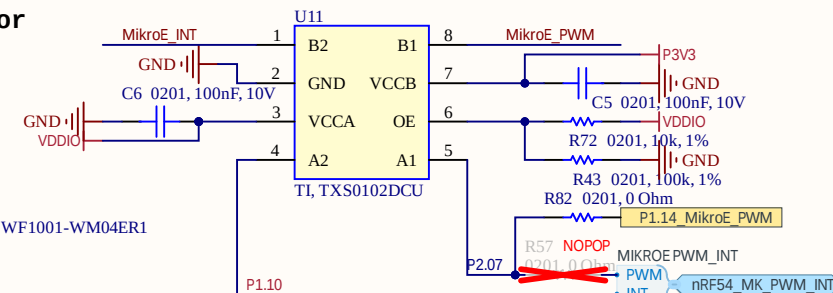
Debugger USB Connector



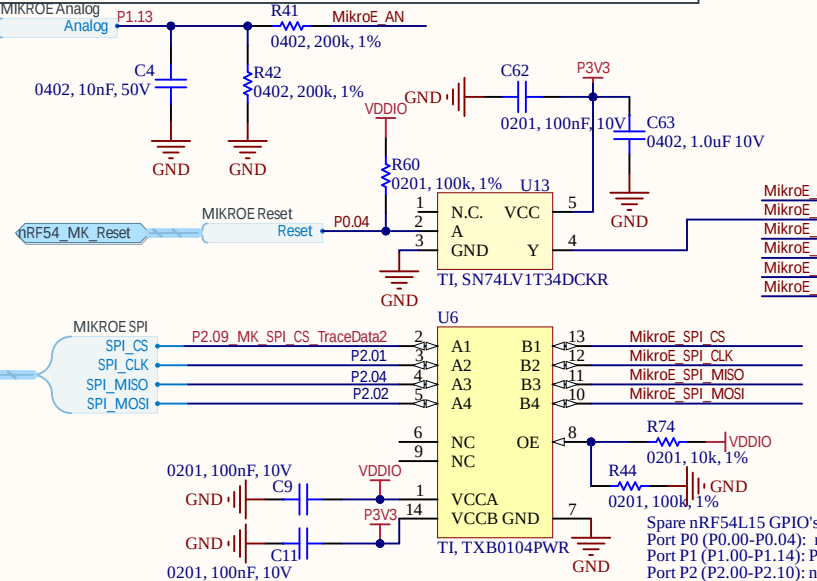
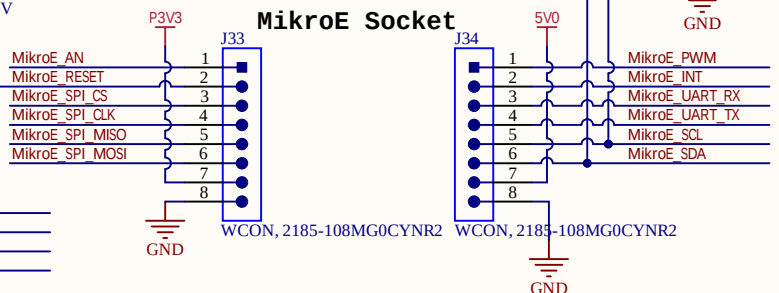
Trace connector



Qwiic connector



MikroE Socket



Title: Schematic, BL54L15u Module Dev Board,, Chip Antenna				
Sheet Name: connectors	Project Number: 941-00032	Assembly Number: 940-00078	Drawing Number: 940-00078-SCH	Rev. 3-0
File: 940-00078-SCH[connectors]SchDoc	Customer: Internal	Last Modified: 6/2/2025 AM 10:32:18	Size: B	Sheet: 8 of 8

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