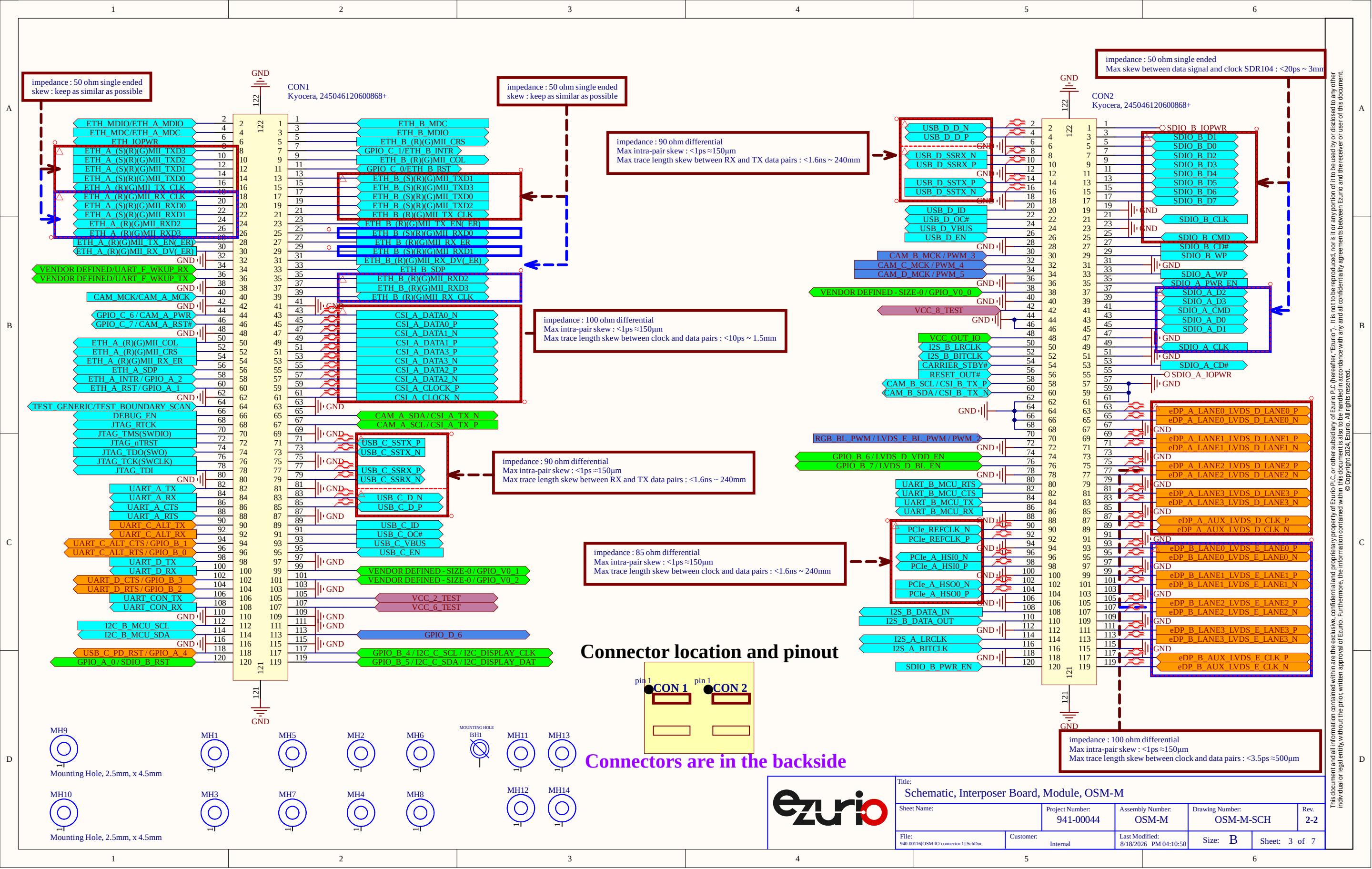
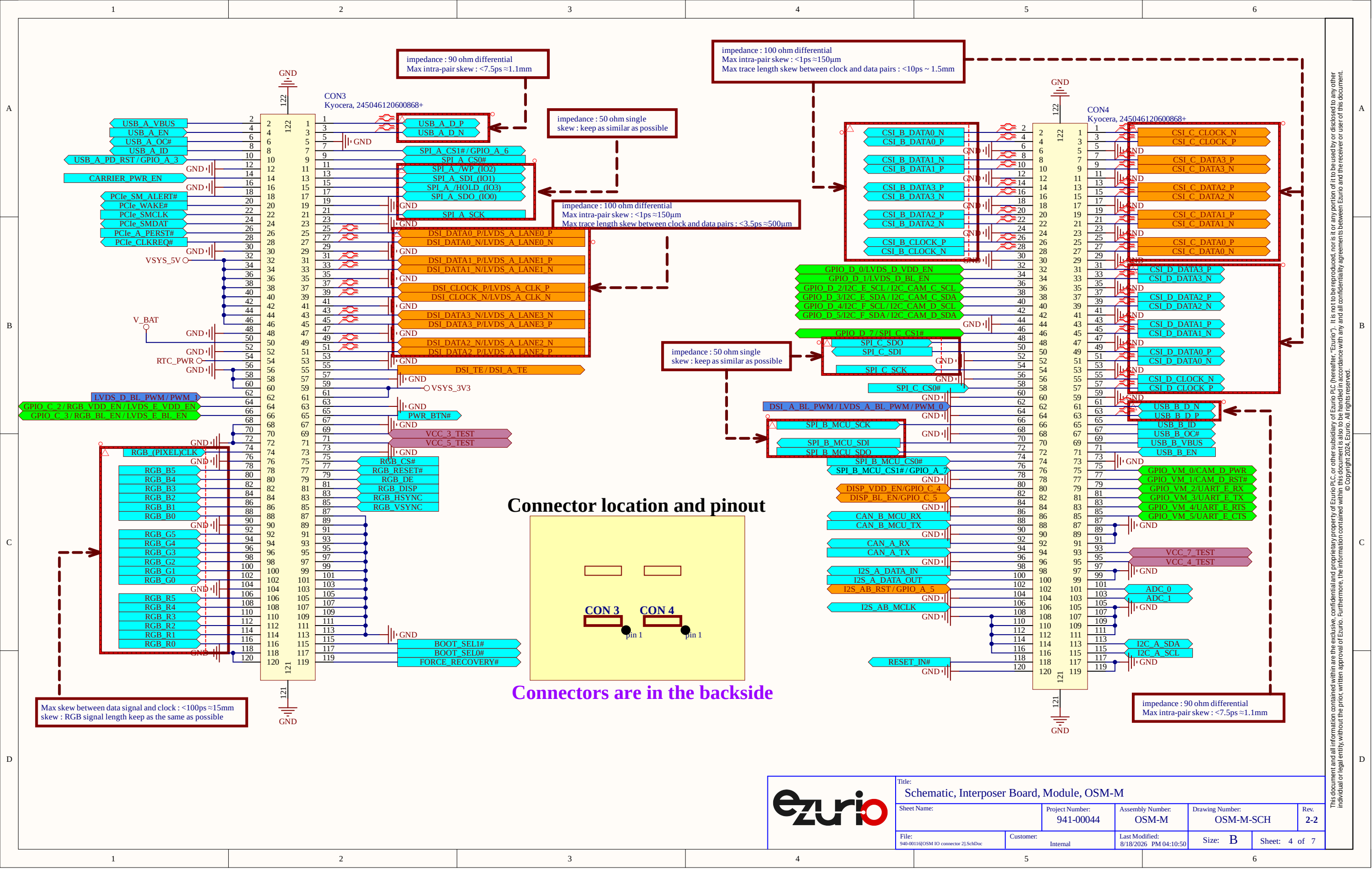






OSM-0

MOD1A		
A15	COM_AREA_01	
A16	COM_AREA_02	
A17	COM_AREA_03	
A18	COM_AREA_04	
A19	COM_AREA_05	
A20	COM_AREA_06	
A21	COM_AREA_07	
B15	COM_AREA_08	
B16	COM_AREA_09	
B17	COM_AREA_10	
B18	COM_AREA_11	
B19	COM_AREA_12	
B20	COM_AREA_13	
B21	COM_AREA_14	
C15	COM_AREA_15	
C17	COM_AREA_16	
C19	COM_AREA_17	
C21	COM_AREA_18	

Ezurio, OSM, Size-0 Zero, Platform Side

GPIO_A_0/SDIO_B_RST	D17
ETH_A_RST/GPIO_A_1	E17
ETH_A_INTR/GPIO_A_2	F17
USB_A_PD_RST/GPIO_A_3	G17
USB_C_PD_RST/GPIO_A_4	H17
I2S_AB_RST/GPIO_A_5	J17
UART_C_ALT_RTS/GPIO_B_0	D19
UART_C_ALT_CTS/GPIO_B_1	E19
UART_D_RTS/GPIO_B_2	F19
UART_D_CTS/GPIO_B_3	G19
GPIO_B_4/I2C_C_SCL/I2C_DISPLAY_CLK	H19
GPIO_B_5/I2C_C_SDA/I2C_DISPLAY_DAT	J19
GPIO_B_6/LVDS_D_VDD_EN	K19
GPIO_B_7/LVDS_D_BL_EN	L19

MOD1D

GPIO_A_0/SDIO_B_RST	D17
GPIO_A_1/ETH_A_RST	E17
GPIO_A_2/ETH_A_INTR	F17
GPIO_A_3/USB_A_PD_RST	G17
GPIO_A_4/USB_C_PD_RST	H17
GPIO_A_5/I2S_AB_RST	J17
GPIO_B_0/UART_C_ALT_RTS	D19
GPIO_B_1/UART_C_ALT_CTS	E19
GPIO_B_2/UART_D_RTS	F19
GPIO_B_3/UART_D_CTS	G19
GPIO_B_4/I2C_C_SCL/I2C_DISPLAY_CLK	H19
GPIO_B_5/I2C_C_SDA/I2C_DISPLAY_DAT	J19
GPIO_B_6/LVDS_D_VDD_EN	K19
GPIO_B_7/LVDS_D_BL_EN	L19

Ezurio, OSM, Size-0 Zero, Platform Side

I2S_A_DATA_IN	V21
I2S_A_DATA_OUT	W21
I2S_B_DATA_IN	V19
I2S_B_DATA_OUT	W19
I2S_AB_MCLK	V18
I2S_A_LRCLK	W18
I2S_A_BITCLK	W20

MOD1H

I2S_A_DATA_IN	V21
I2S_A_DATA_OUT	W21
I2S_B_DATA_IN	V19
I2S_B_DATA_OUT	W19
I2S_MCLK/I2S_AB_MCLK	V18
I2S_A_LRCLK	W18
I2S_A_BITCLK	W20

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Name + Signal Name Changed

CAN_A_TX	AC17
CAN_A_RX	AB17
CAN_B_MCU_TX	AC19
CAN_B_MCU_RX	AB19

MOD1I

CAN_A_TX	AC17
CAN_A_RX	AB17
CAN_B_TX/CAN_B_MCU_TX	AC19
CAN_B_RX/CAN_B_MCU_RX	AB19

Ezurio, OSM, Size-0 Zero, Platform Side

USB_A_D_N	AB13
USB_A_D_P	AC14
USB_A_ID	AB14
USB_A_OC#	AC15
USB_A_VBUS	AB16
USB_A_EN	AC16
USB_B_D_N	AB23
USB_B_D_P	AC22
USB_B_ID	AB22
USB_B_OC#	AC21
USB_B_VBUS	AB20
USB_B_EN	AC20

MOD1J

USB_A_D_N	AB13
USB_A_D_P	AC14
USB_A_ID	AB14
USB_A_OC#	AC15
USB_A_VBUS	AB16
USB_A_EN	AC16
USB_B_D_N	AB23
USB_B_D_P	AC22
USB_B_ID	AB22
USB_B_OC#	AC21
USB_B_VBUS	AB20
USB_B_EN	AC20

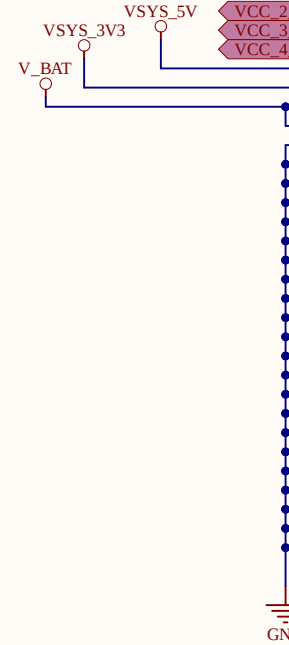
Ezurio, OSM, Size-0 Zero, Platform Side

I2C_A_SCL	AA15
I2C_A_SDA	AA16
I2C_B_MCU_SCL	AA20
I2C_B_MCU_SDA	AA21

MOD1K

I2C_A_SCL	AA15
I2C_A_SDA	AA16
I2C_B_SCL/I2C_B_MCU_SCL	AA20
I2C_B_SDA/I2C_B_MCU_SDA	AA21

Ezurio, OSM, Size-0 Zero, Platform Side



MOD1N		
VCC_2_TEST2	M19	
VCC_3_TEST	Y16	
VCC_4_TEST	Y20	
VCC_IN_5V	Y17	
VCC_IN_3V3	Y19	
V_BAT	AA18	
V_BAT	AB18	
GND	D18	
GND	E15	
GND	E21	
GND	F16	
GND	F20	
GND	J16	
GND	J20	
GND	L18	
GND	M16	
GND	M20	
GND	P18	
GND	R16	
GND	R20	
GND	V16	
GND	V20	
GND	Y18	
GND	AA14	
GND	AA17	
GND	AA19	
GND	AA22	
GND	AB15	
GND	AB21	

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Name + Signal Name Changed

RESET_IN#	U17
CARRIER_PWR_EN	V17
VCC_OUT_IO	U18
RTC_PWR_O	W17
BOOT_SELO#	U19
BOOT_SEL1#	R18
FORCE_RECOVERY#	T17

MOD1P

RESET_IN#	U17
CARRIER_PWR_EN	V17
VCC_OUT_IO	U18
RTC_PWR	W17
BOOT_SELO#	U19
BOOT_SEL1#	R18
FORCE_RECOVERY#	T17

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Names Changed

ADC_0	M18
ADC_1	N18

MOD1L

ADC_0	M18
ADC_1	N18

Ezurio, OSM, Size-0 Zero, Platform Side

I2S_B_LRCLK	T18
I2S_B_BITCLK	T19
CARRIER_STBY#	Y13
RESET_OUT#	Y14

MOD1O

I2S_B_LRCLK	T18
I2S_B_BITCLK	T19
CARRIER_STBY#	Y13
RESET_OUT#	Y14
RESERVED	AA13
VENDOR DEFINED - SIZE-0 / GPIO_V0_0	B22
VENDOR DEFINED - SIZE-0 / GPIO_V0_1	C16
VENDOR DEFINED - SIZE-0 / GPIO_V0_2	P16

Ezurio, OSM, Size-0 Zero, Platform Side

Signal Order, Pin Name, Signal Name Changed

SPI_A_SDO_(IO0)	V15
SPI_A_SDI_(IO1)	U15
SPI_A_WP_(IO2)	W16
SPI_A_HOLD_(IO3)	W15
SPI_A_CS0#	Y15
SPI_A_CS1#/GPIO_A_6	K17
SPI_A_SCK	U16
SPI_B_MCU_SDI	Y22
SPI_B_MCU_SDO	Y23
SPI_B_MCU_CS0#	AA23
SPI_B_MCU_CS1#/GPIO_A_7	L17
SPI_B_MCU_SCK	Y21

MOD1G

SPI_A_SDO_(IO0)	V15
SPI_A_SDI_(IO1)	U15
SPI_A_WP_(IO2)	W16
SPI_A_HOLD_(IO3)	W15
SPI_A_CS0#	Y15
SPI_A_CS1#/GPIO_A_6	K17
SPI_A_SCK	U16
SPI_B_SDI/SPI_B_MCU_SDI	Y22
SPI_B_SDO/SPI_B_MCU_SDO	Y23
SPI_B_CS0#/SPI_B_MCU_CS0#	AA23
SPI_B_CS1#/GPIO_A_7	L17
SPI_B_SCK/SPI_B_MCU_SCK	Y21

Ezurio, OSM, Size-0 Zero, Platform Side

JTAG_TCK(SWCLK)	N17
JTAG_TMS(SWDIO)	N19
JTAG_TDI	P17
JTAG_RTCK	P19
JTAG_TDO(SWO)	R17
JTAG_nTRST	R19
DEBUG_EN	AC18
TEST_GENERIC/TEST_BOUNDARY_SCAN	C18

MOD1M

JTAG_TCK(SWCLK)	N17
JTAG_TMS(SWDIO)	N19
JTAG_TDI	P17
JTAG_RTCK	P19
JTAG_TDO(SWO)	R17
JTAG_nTRST	R19
DEBUG_EN	AC18
TEST_GENERIC/TEST_BOUNDARY_SCAN	C18

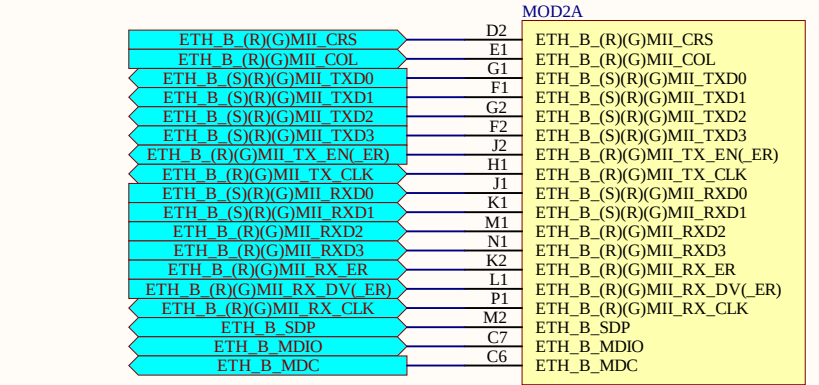
Ezurio, OSM, Size-0 Zero, Platform Side



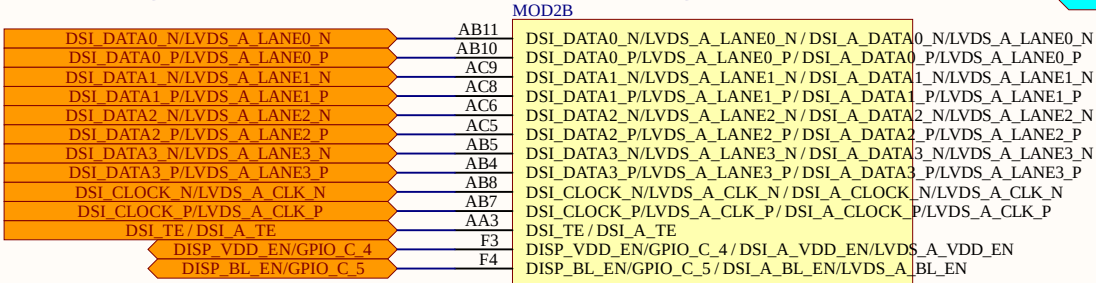
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Sheet Name: OSM-0 pinout	Project Number: 941-00044	Assembly Number: OSM-M	Drawing Number: OSM-M-SCH	Rev. 2-2
File: 940-00116(OSM-0 pinout)SchDoc	Customer: Internal	Last Modified: 8/18/2026 PM 04:10:51	Size: B	Sheet: 5 of 7

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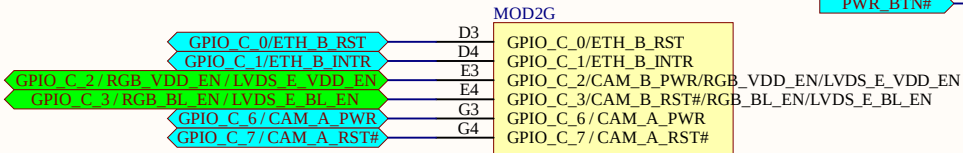
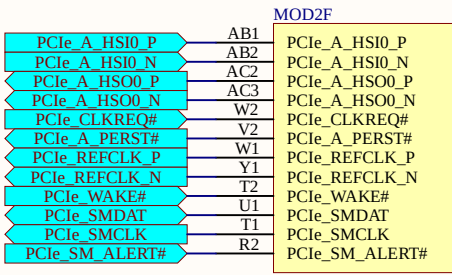
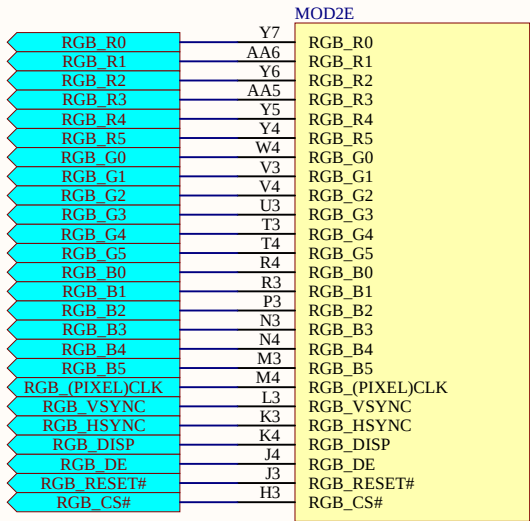
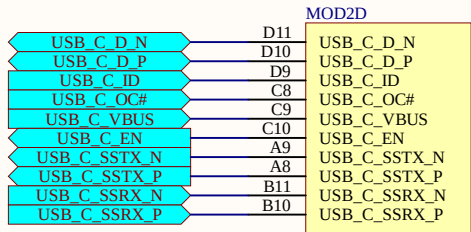
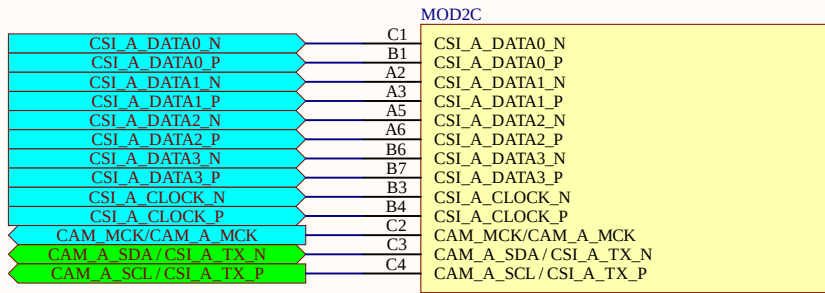
OSM-S



Signal Name + Pin Names Changed

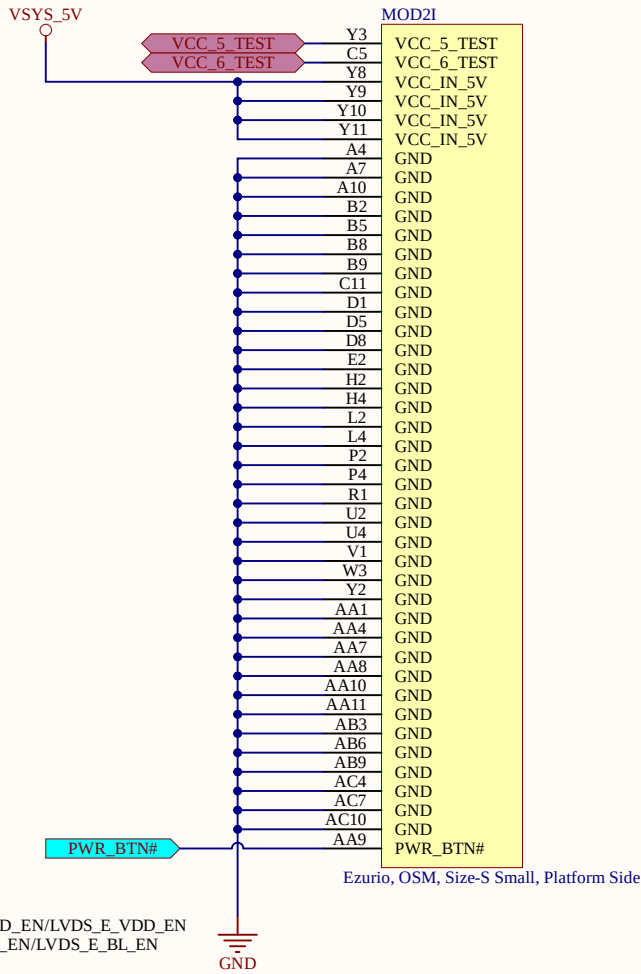
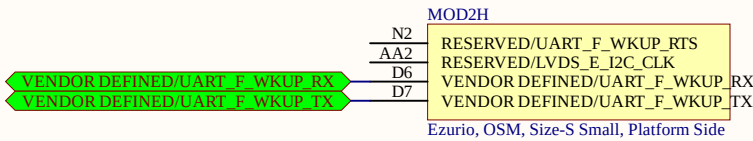


Signal Name + Pin Names Changed



Pin Names Changed

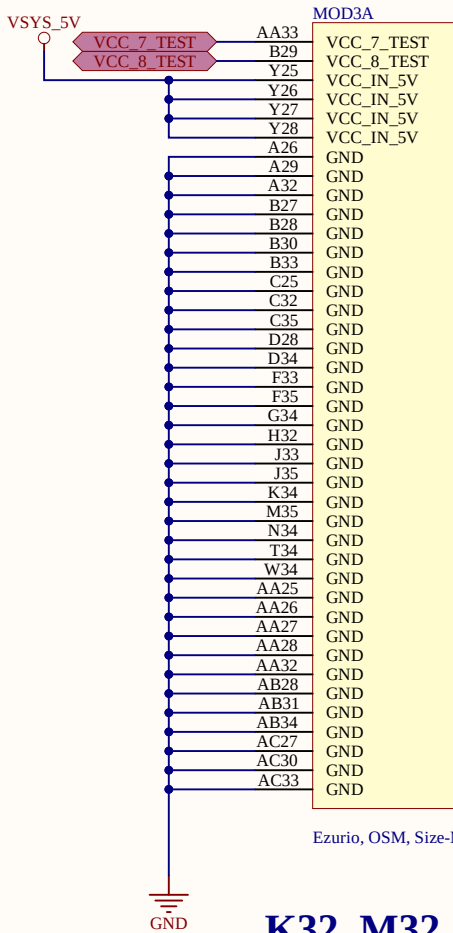
Signal Name + Pin Names Changed



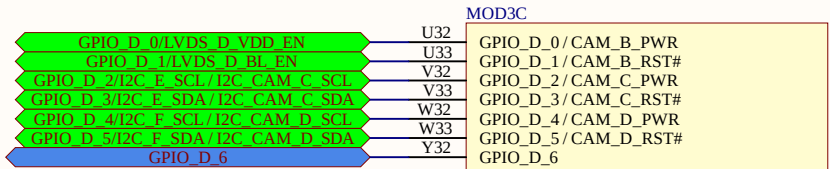
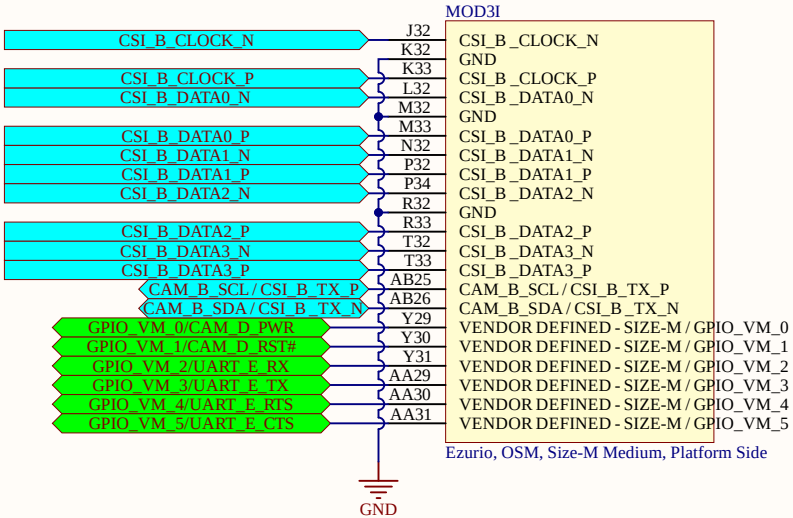
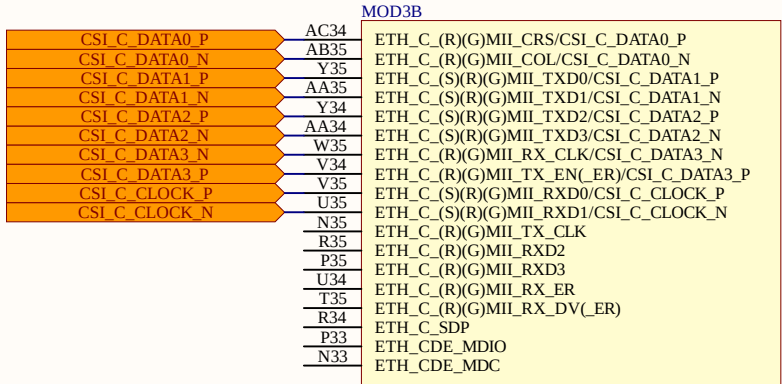
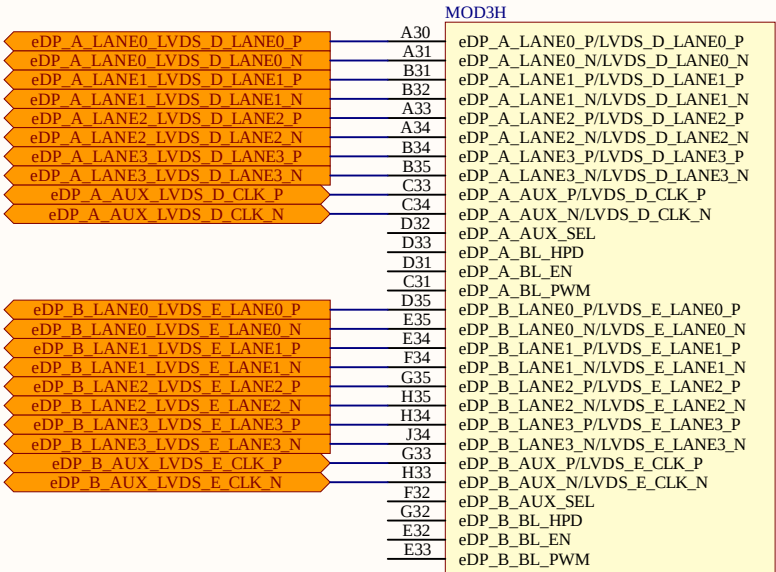
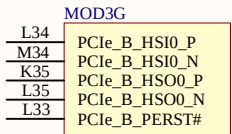
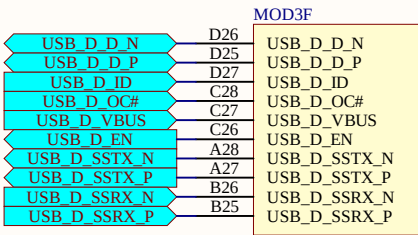
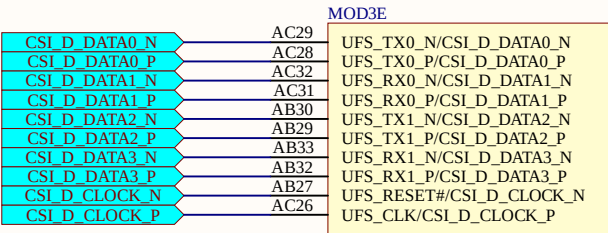
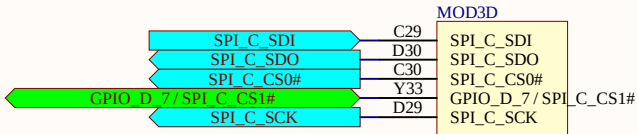
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Sheet Name: OSM-S pinout	Project Number: 941-00044	Assembly Number: OSM-M	Drawing Number: OSM-M-SCH	Rev. 2-2
File: 940-00116(OSM-S pinout).SchDoc	Customer: Internal	Last Modified: 8/18/2026 PM 04:10:51	Size: B	Sheet: 6 of 7

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OSM-M



K32, M32, R32Added to GND



K32, M32, R32 Removed + Signal order + Pin Names + Signal Names chnaged

Signal Name + Pin Names Changed

	Title: Schematic, Interposer Board, Module, OSM-M			
	Sheet Name: OSM-M pinout	Project Number: 941-00044	Assembly Number: OSM-M	Drawing Number: OSM-M-SCH
File: 940-00116[OSM-M pinout].SchDoc	Customer: Internal	Last Modified: 8/18/2026 PM 04:10:51	Size: B	Rev. 2-2
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