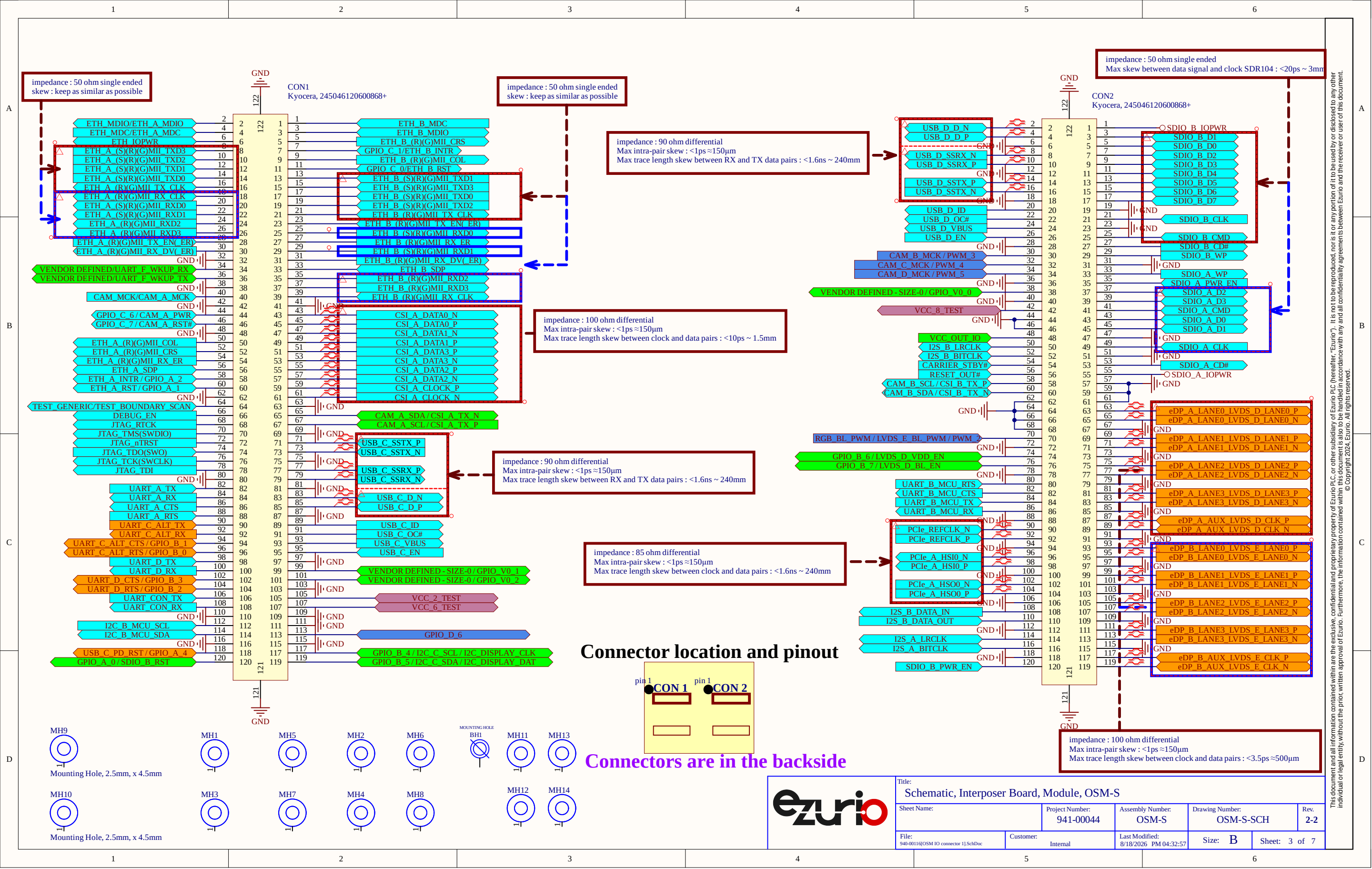
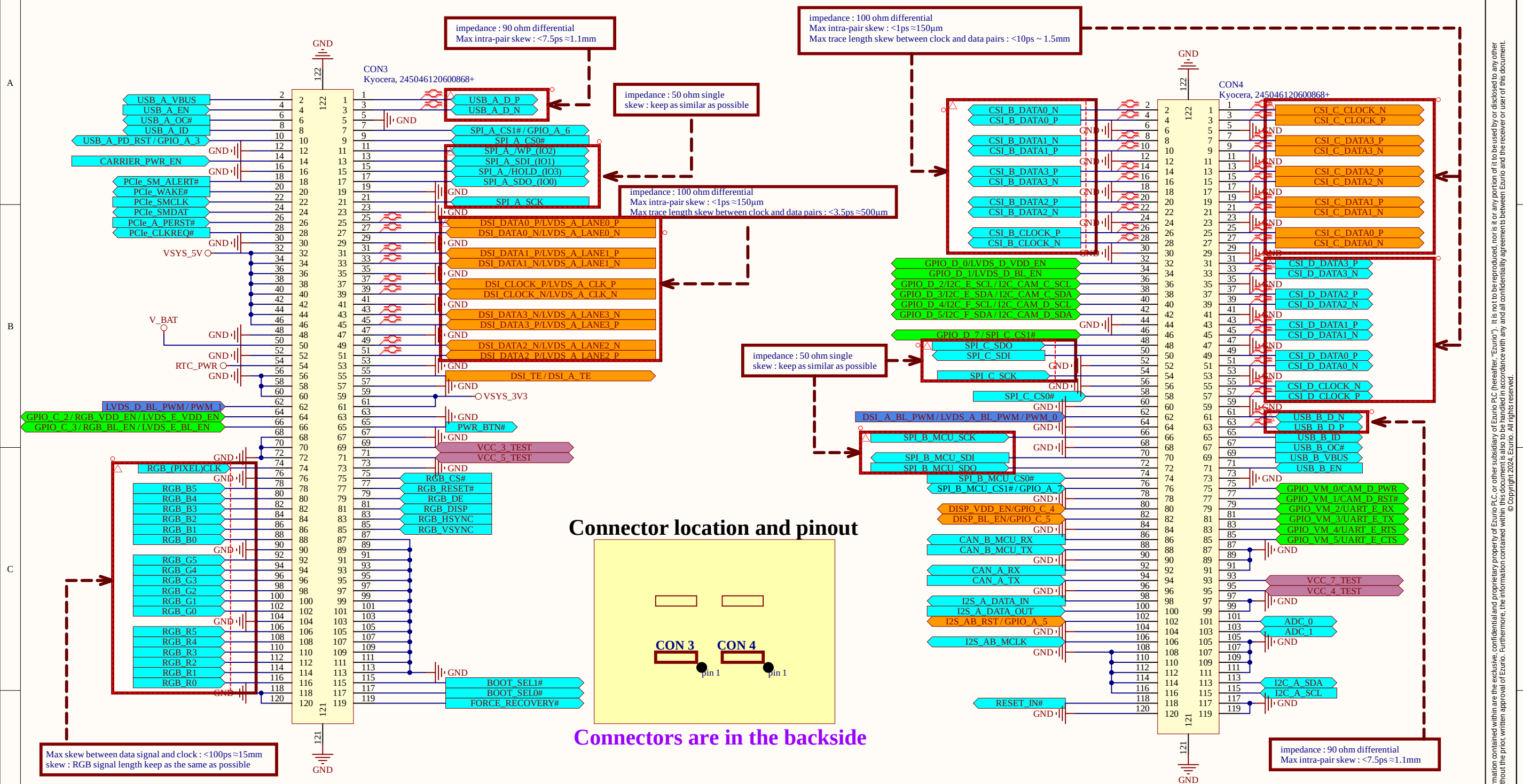


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<table><tr><td>Revised Date</td><td>Reversion</td><td>Modified By</td><td>Note</td></tr><tr><td>2024/09/12</td><td>00</td><td>Louis Chang</td><td>Release for Design review</td></tr><tr><td>2025/01/22</td><td>1-0</td><td></td><td>Initial release</td></tr><tr><td>2025/07/02</td><td>2-0</td><td>Andrew Chen</td><td>Added four holes for OSM heat sink</td></tr><tr><td>2025/10/03</td><td>2-1</td><td>Louis Chang</td><td>1. OSM pin name changed 1.1 RGB_BL_PWM / PWM_1 change to LVDS_D_BL_PWM / PWM_1 1.2 LVDS_D_BL_PWM / PWM_2 change to RGB_BL_PWM / LVDS_E_BL_PWM / PWM_2 1.3 LVDS_E_BL_PWM / PWM_3 change to CAM_B_MCK / PWM_3 1.4 CAM_B_MCK / PWM_4 change to CAM_C_MCK / PWM_4 1.5 CAM_C_MCK / PWM_5 change to CAM_D_MCK / PWM_5 1.6 CAM_D_MCK / GPIO_D_6 change to GPIO_D_6</td></tr><tr><td>2026/05/27</td><td>2-2</td><td>AL</td><td>Modify the project name to "Carbon AMxx" (with a space)</td></tr><tr><td colspan="4"></td></tr><tr><td colspan="4"></td></tr><tr><td colspan="4"></td></tr><tr><td colspan="4"></td></tr></table>												Revised Date	Reversion	Modified By	Note	2024/09/12	00	Louis Chang	Release for Design review	2025/01/22	1-0		Initial release	2025/07/02	2-0	Andrew Chen	Added four holes for OSM heat sink	2025/10/03	2-1	Louis Chang	1. OSM pin name changed 1.1 RGB_BL_PWM / PWM_1 change to LVDS_D_BL_PWM / PWM_1 1.2 LVDS_D_BL_PWM / PWM_2 change to RGB_BL_PWM / LVDS_E_BL_PWM / PWM_2 1.3 LVDS_E_BL_PWM / PWM_3 change to CAM_B_MCK / PWM_3 1.4 CAM_B_MCK / PWM_4 change to CAM_C_MCK / PWM_4 1.5 CAM_C_MCK / PWM_5 change to CAM_D_MCK / PWM_5 1.6 CAM_D_MCK / GPIO_D_6 change to GPIO_D_6	2026/05/27	2-2	AL	Modify the project name to "Carbon AMxx" (with a space)																																		
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PCB1 Ezurio, 750-00044																																																																					
<table><tr><td colspan="2">Approvals</td><td colspan="2">Date</td><td colspan="6" rowspan="4"> ezurio 3F-1, No.145, Xianzheng 9th Rd, Zhubei City, Hsinchu County 30251, Taiwan (R.O.C.) Phone: +886-3-552-9695 www.ezurio.com </td></tr><tr><td colspan="2">Drawn: LC, AL</td><td colspan="2">09/02/2025</td></tr><tr><td colspan="2">Checked: AC, LC</td><td colspan="2">09/05/2025</td></tr><tr><td colspan="2">Approved: AC</td><td colspan="2">09/05/2025</td></tr><tr><td colspan="8">Title: Schematic, Interposer Board, Module, OSM-S</td><td colspan="4"></td></tr><tr><td colspan="4">Sheet Name: History</td><td colspan="2">Project Number: 941-00044</td><td colspan="2">Assembly Number: OSM-S</td><td colspan="2">Drawing Number: OSM-S-SCH</td><td colspan="2">Rev. 2-2</td></tr><tr><td colspan="4">File: 940-00116[History].SchDoc</td><td colspan="2">Customer: Internal</td><td colspan="2">Last Modified: 8/18/2026 PM 04:32:57</td><td colspan="2">Size: B</td><td colspan="2">Sheet: 1 of 7</td></tr></table>												Approvals		Date		ezurio 3F-1, No.145, Xianzheng 9th Rd, Zhubei City, Hsinchu County 30251, Taiwan (R.O.C.) Phone: +886-3-552-9695 www.ezurio.com						Drawn: LC, AL		09/02/2025		Checked: AC, LC		09/05/2025		Approved: AC		09/05/2025		Title: Schematic, Interposer Board, Module, OSM-S												Sheet Name: History				Project Number: 941-00044		Assembly Number: OSM-S		Drawing Number: OSM-S-SCH		Rev. 2-2		File: 940-00116[History].SchDoc				Customer: Internal		Last Modified: 8/18/2026 PM 04:32:57		Size: B		Sheet: 1 of 7	
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Connectors are in the backside

OSM-0

MOD1A		
A15	COM_AREA_01	
A16	COM_AREA_02	
A17	COM_AREA_03	
A18	COM_AREA_04	
A19	COM_AREA_05	
A20	COM_AREA_06	
A21	COM_AREA_07	
B15	COM_AREA_08	
B16	COM_AREA_09	
B17	COM_AREA_10	
B18	COM_AREA_11	
B19	COM_AREA_12	
B20	COM_AREA_13	
B21	COM_AREA_14	
C15	COM_AREA_15	
C17	COM_AREA_16	
C19	COM_AREA_17	
C21	COM_AREA_18	

Ezurio, OSM, Size-0 Zero, Platform Side

GPIO_A_0/SDIO_B_RST	D17
ETH_A_RST/GPIO_A_1	E17
ETH_A_INTR/GPIO_A_2	F17
USB_A_PD_RST/GPIO_A_3	G17
USB_C_PD_RST/GPIO_A_4	H17
I2S_AB_RST/GPIO_A_5	J17
UART_C_ALT_RTS/GPIO_B_0	D19
UART_C_ALT_CTS/GPIO_B_1	E19
UART_D_RTS/GPIO_B_2	F19
UART_D_CTS/GPIO_B_3	G19
GPIO_B_4/I2C_C_SCL/I2C_DISPLAY_CLK	H19
GPIO_B_5/I2C_C_SDA/I2C_DISPLAY_DAT	J19
GPIO_B_6/LVDS_D_VDD_EN	K19
GPIO_B_7/LVDS_D_BL_EN	L19

MOD1D		
GPIO_A_0/SDIO_B_RST	D17	
GPIO_A_1/ETH_A_RST	E17	
GPIO_A_2/ETH_A_INTR	F17	
GPIO_A_3/USB_A_PD_RST	G17	
GPIO_A_4/USB_C_PD_RST	H17	
GPIO_A_5/I2S_AB_RST	J17	
GPIO_B_0/UART_C_ALT_RTS	D19	
GPIO_B_1/UART_C_ALT_CTS	E19	
GPIO_B_2/UART_D_RTS	F19	
GPIO_B_3/UART_D_CTS	G19	
GPIO_B_4/I2C_C_SCL/I2C_DISPLAY_CLK	H19	
GPIO_B_5/I2C_C_SDA/I2C_DISPLAY_DAT	J19	
GPIO_B_6/LVDS_D_VDD_EN	K19	
GPIO_B_7/LVDS_D_BL_EN	L19	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1H		
I2S_A_DATA_IN	V21	
I2S_A_DATA_OUT	W21	
I2S_B_DATA_IN	V19	
I2S_B_DATA_OUT	W19	
I2S_AB_MCLK	V18	
I2S_A_LRCLK	W18	
I2S_A_BITCLK	W20	

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Name + Signal Name Changed

MOD1I		
CAN_A_TX	AC17	
CAN_A_RX	AB17	
CAN_B_MCU_TX	AC19	
CAN_B_MCU_RX	AB19	

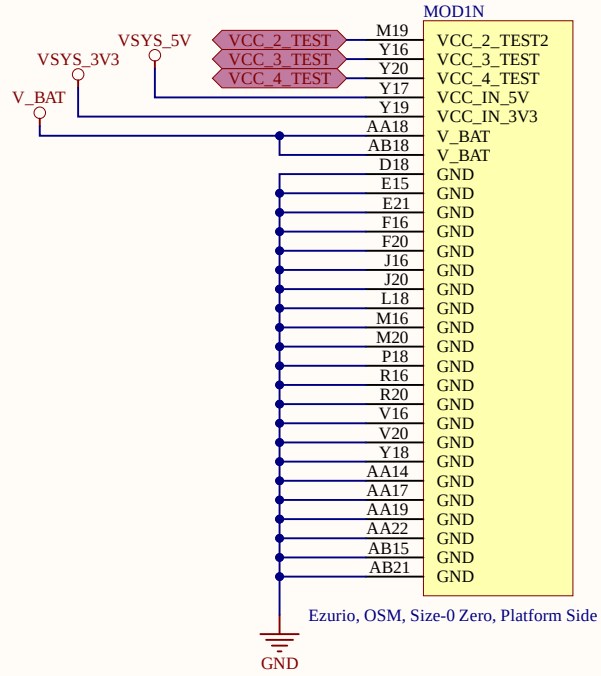
Ezurio, OSM, Size-0 Zero, Platform Side

MOD1J		
USB_A_D_N	AB13	
USB_A_D_P	AC14	
USB_A_ID	AB14	
USB_A_OC#	AC15	
USB_A_VBUS	AB16	
USB_A_EN	AC16	
USB_B_D_N	AB23	
USB_B_D_P	AC22	
USB_B_ID	AB22	
USB_B_OC#	AC21	
USB_B_VBUS	AB20	
USB_B_EN	AC20	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1K		
I2C_A_SCL	AA15	
I2C_A_SDA	AA16	
I2C_B_MCU_SCL	AA20	
I2C_B_MCU_SDA	AA21	

Ezurio, OSM, Size-0 Zero, Platform Side



Ezurio, OSM, Size-0 Zero, Platform Side

Pin Name + Signal Name Changed

MOD1P		
RESET_IN#	U17	
CARRIER_PWR_EN	V17	
VCC_OUT_IO	U18	
RTC_PWR_O	W17	
BOOT_SELO#	U19	
BOOT_SEL1#	R18	
FORCE_RECOVERY#	T17	

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Names Changed

MOD1L		
ADC_0	M18	
ADC_1	N18	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1O		
I2S_B_LRCLK	T18	
I2S_B_BITCLK	T19	
CARRIER_STBY#	Y13	
RESET_OUT#	Y14	
RESERVED	AA13	
VENDOR DEFINED - SIZE-0 / GPIO_V0_0	B22	
VENDOR DEFINED - SIZE-0 / GPIO_V0_1	C16	
VENDOR DEFINED - SIZE-0 / GPIO_V0_2	P16	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1B		
UART_A_RX	A14	
UART_A_TX	B13	
UART_A_RTS	C13	
UART_A_CTS	C14	
UART_B_RX/UART_B_MCU_RX	D14	
UART_B_MCU_TX	D13	
UART_B_MCU_RTS	D15	
UART_B_MCU_CTS	D16	
UART_C_RX/UART_C_ALT_RX	A22	
UART_C_ALT_TX	B23	
UART_D_RX	C22	
UART_D_TX	C23	
UART_CON_RX	D22	
UART_CON_TX	D23	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1E		
ETH_A_(R)(G)MII_CRS	E16	
ETH_A_(R)(G)MII_COL	F15	
ETH_A_(S)(R)(G)MII_TXD0	H15	
ETH_A_(S)(R)(G)MII_TXD1	G15	
ETH_A_(S)(R)(G)MII_TXD2	H16	
ETH_A_(S)(R)(G)MII_TXD3	G16	
ETH_A_(R)(G)MII_TX_EN(ER)	K16	
ETH_A_(R)(G)MII_TX_CLK	J15	
ETH_A_(S)(R)(G)MII_RXD0	K15	
ETH_A_(S)(R)(G)MII_RXD1	L15	
ETH_A_(R)(G)MII_RXD2	N15	
ETH_A_(R)(G)MII_RXD3	P15	
ETH_A_(R)(G)MII_RX_ER	L16	
ETH_A_(R)(G)MII_RX_DV(ER)	M15	
ETH_A_(R)(G)MII_RX_CLK	R15	
ETH_A_SDP	N16	
ETH_MDIO/ETH_A_MDIO	T15	
ETH_MDC/ETH_A_MDC	T16	
ETH_IOPWR	M17	

Ezurio, OSM, Size-0 Zero, Platform Side

Pin Name + Signal Name Changed

MOD1F		
PWM_0/DSI_A_E_BL_PWM/LVDS_A_BL_PWM	E18	
PWM_1/RGB_BL_PWM	F18	
PWM_2/LVDS_D_BL_PWM	G18	
PWM_3/LVDS_E_BL_PWM	H18	
PWM_4/CAM_B_MCK	J18	
PWM_5/CAM_C_MCK	K18	

Ezurio, OSM, Size-0 Zero, Platform Side

Signal Order, Pin Name, Signal Name Changed

MOD1G		
SPI_A_SDO_(IO0)	V15	
SPI_A_SDI_(IO1)	U15	
SPI_A_WP_(IO2)	W16	
SPI_A_HOLD_(IO3)	W15	
SPI_A_CS0#	Y15	
SPI_A_CS1#/GPIO_A_6	K17	
SPI_A_SCK	U16	
SPI_B_MCU_SDI	Y22	
SPI_B_MCU_SDO	Y23	
SPI_B_MCU_CS0#	AA23	
SPI_B_MCU_CS1#/GPIO_A_7	L17	
SPI_B_MCU_SCK	Y21	

Ezurio, OSM, Size-0 Zero, Platform Side

MOD1M		
JTAG_TCK(SWCLK)	N17	
JTAG_TMS(SWDIO)	N19	
JTAG_TDI	P17	
JTAG_RTCK	P19	
JTAG_TDO(SWO)	R17	
JTAG_nTRST	R19	
DEBUG_EN	AC18	
TEST_GENERIC/TEST_BOUNDARY_SCAN	C18	

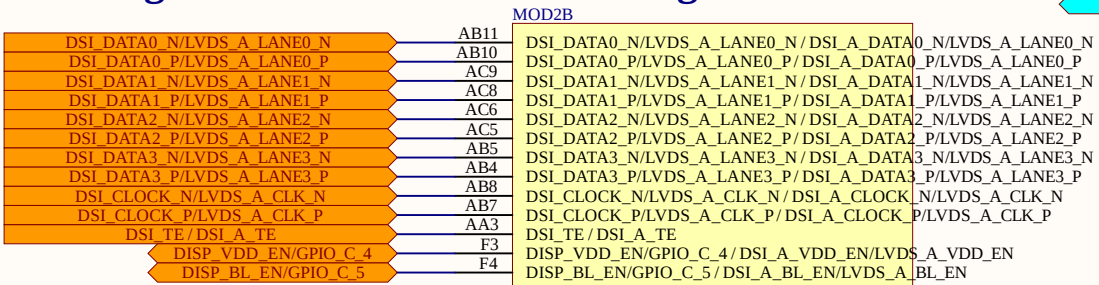
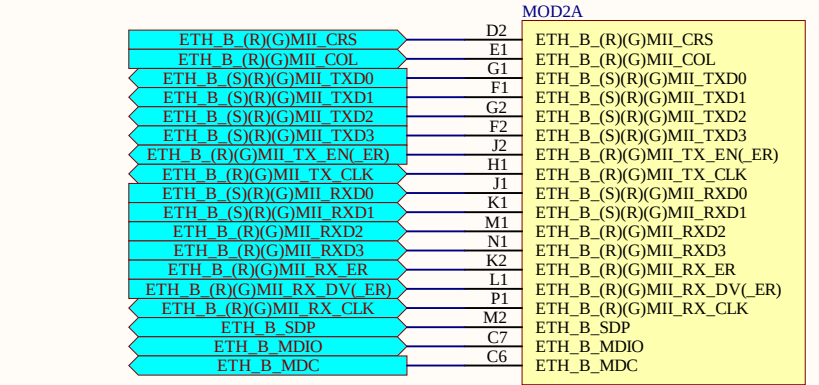
Ezurio, OSM, Size-0 Zero, Platform Side



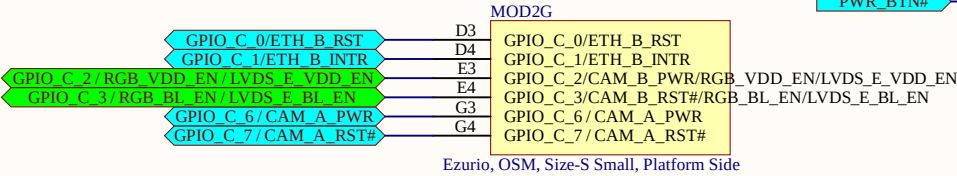
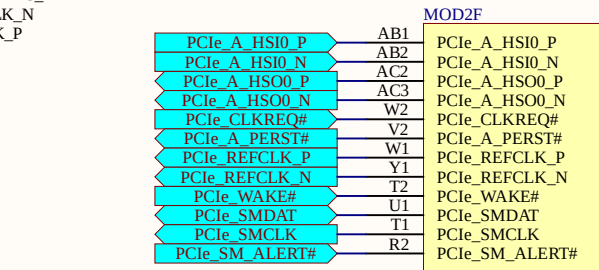
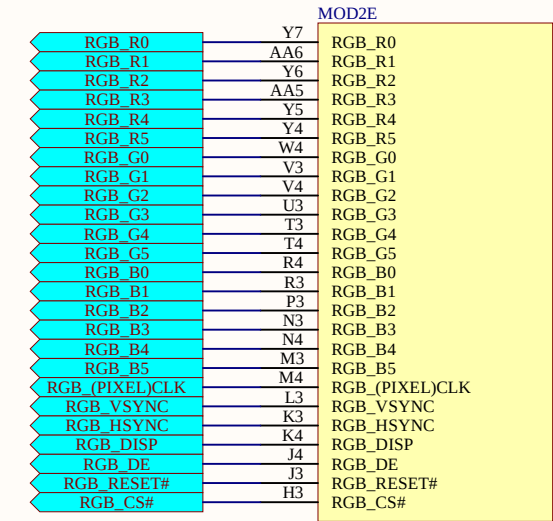
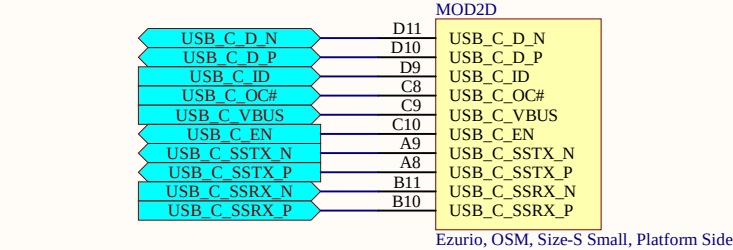
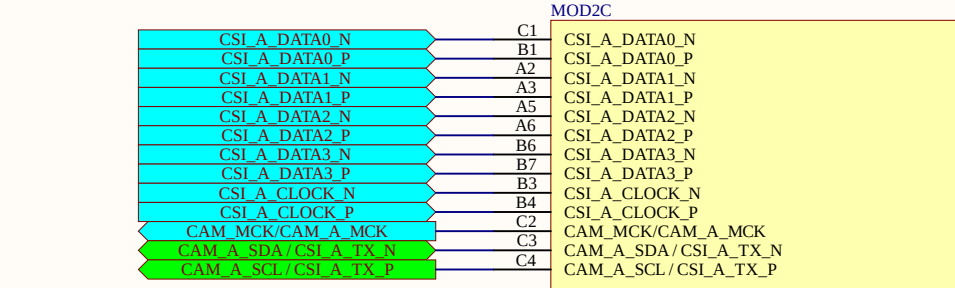
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File: 940-00116(OSM-0 pinout)SchDoc	Customer: Internal	Last Modified: 8/18/2026 PM 04:32:57	Size: B	Sheet: 5 of 7

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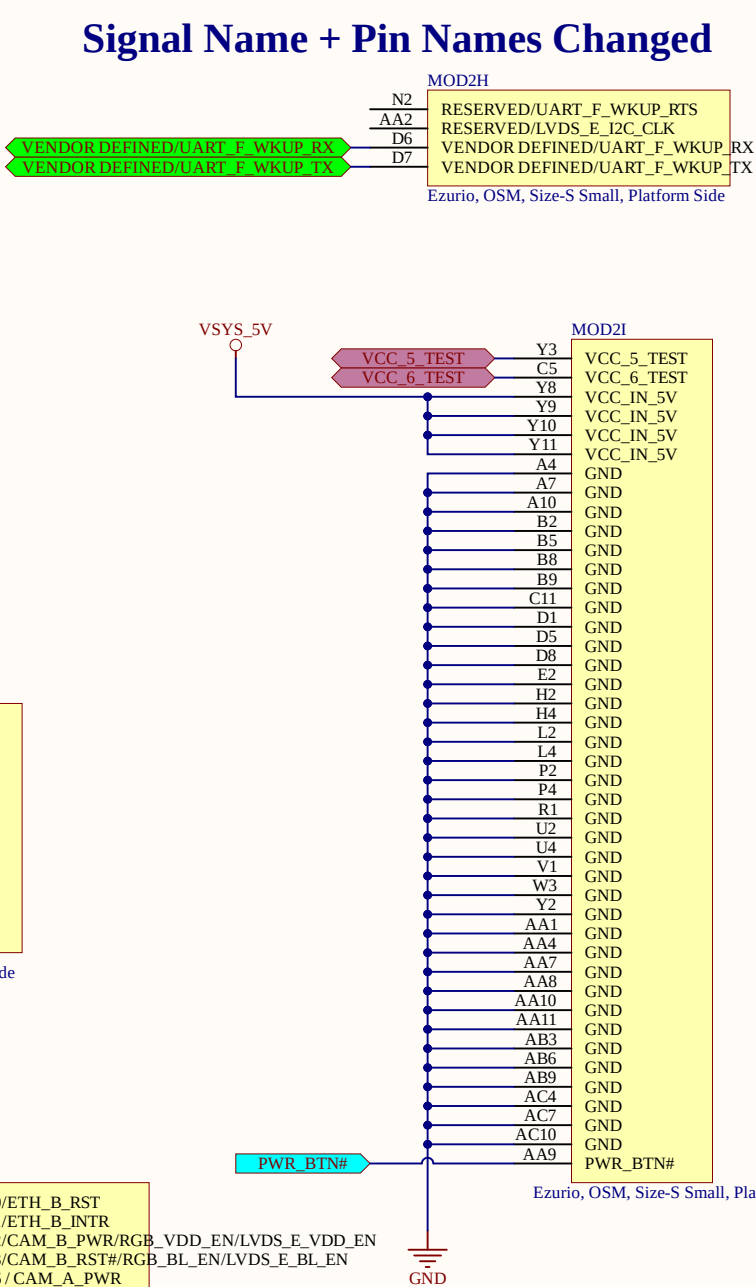
OSM-S



Signal Name + Pin Names Changed



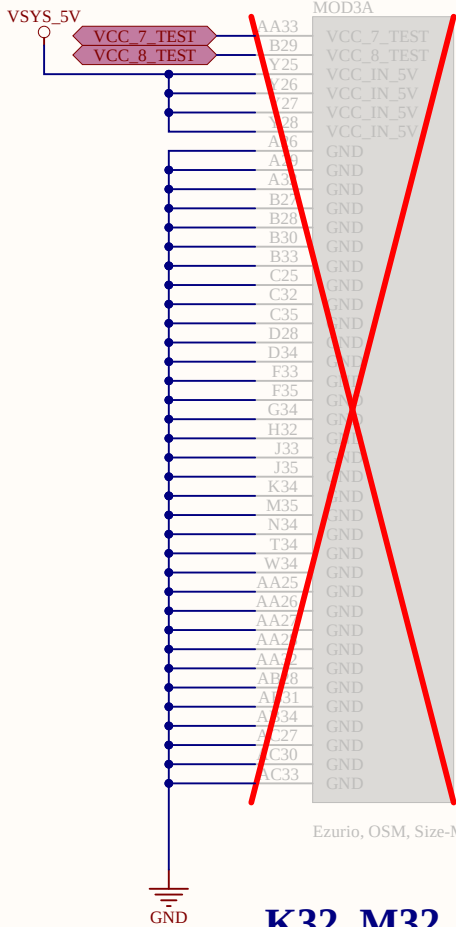
Pin Names Changed



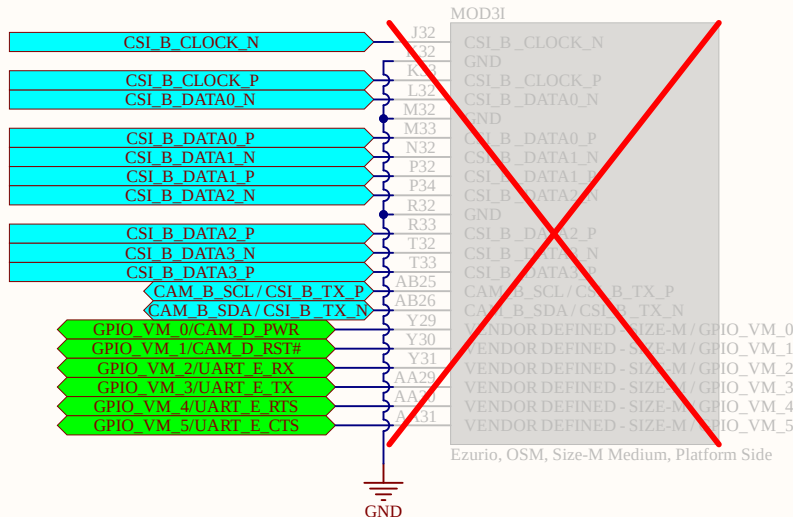
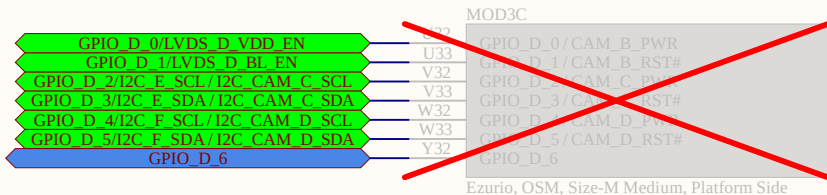
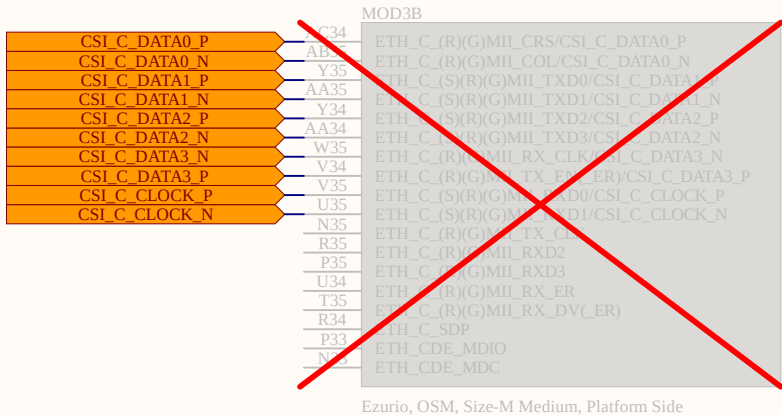
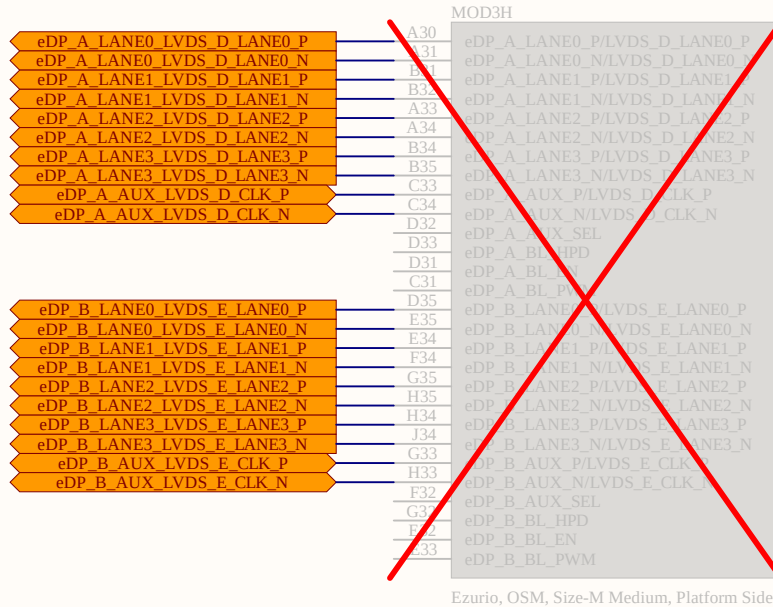
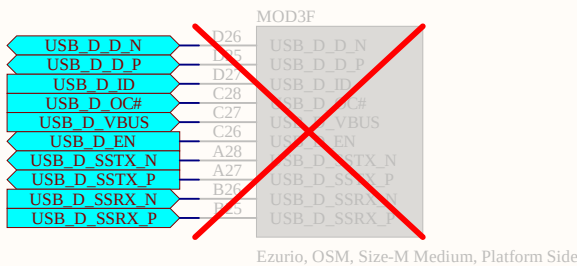
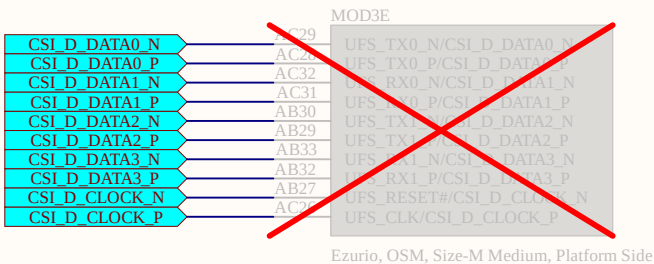
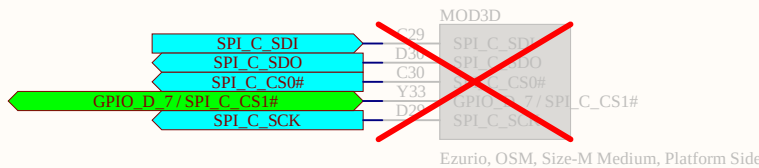
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		Sheet: 6 of 7		Rev: 2-2	

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OSM-M



K32, M32, R32 Added to GND



K32, M32, R32 Removed + Signal order + Pin Names + Signal Names chnaged

Signal Name + Pin Names Changed

	Title: Schematic, Interposer Board, Module, OSM-S				
	Sheet Name: OSM-M pinout	Project Number: 941-00044	Assembly Number: OSM-S	Drawing Number: OSM-S-SCH	Rev. 2-2
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