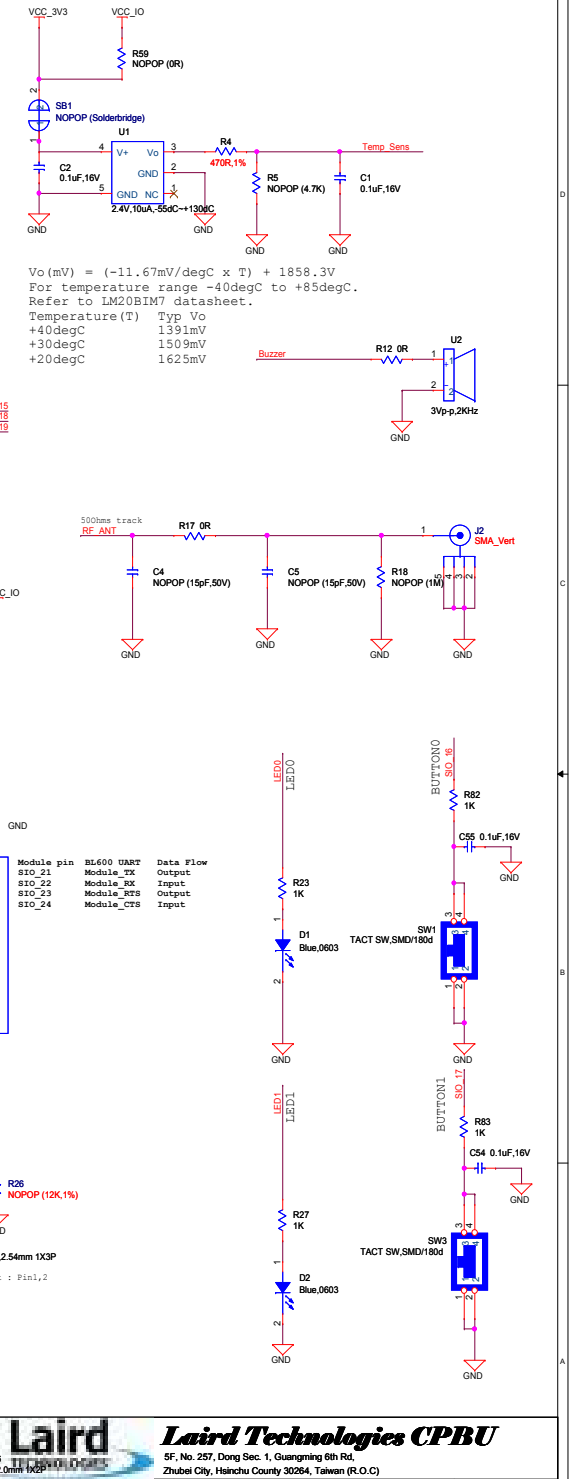
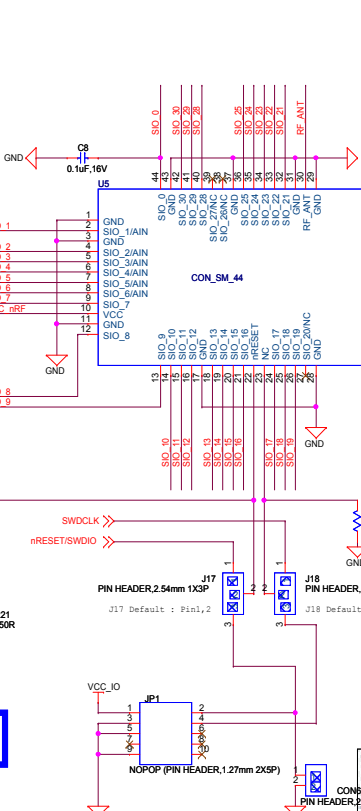
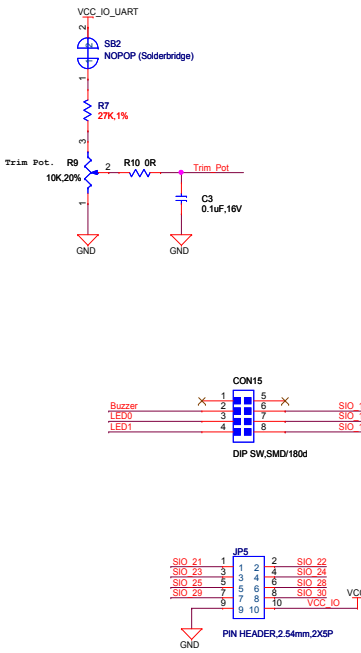
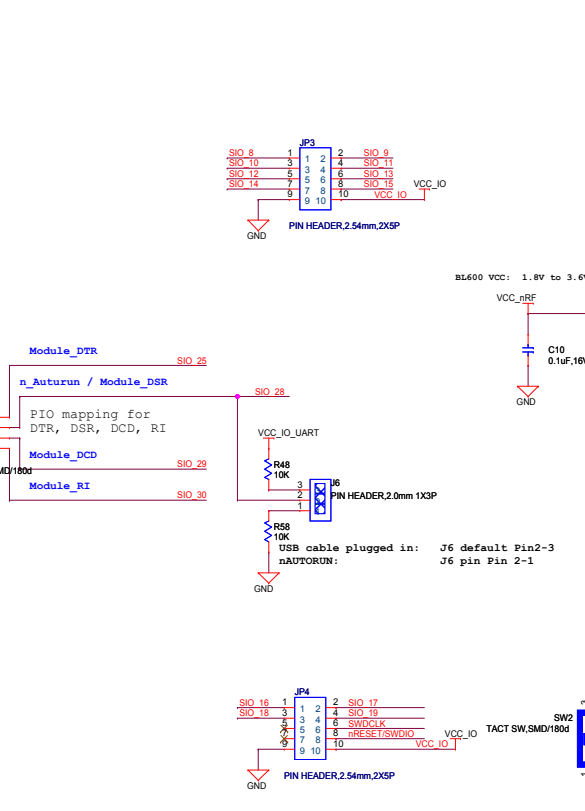
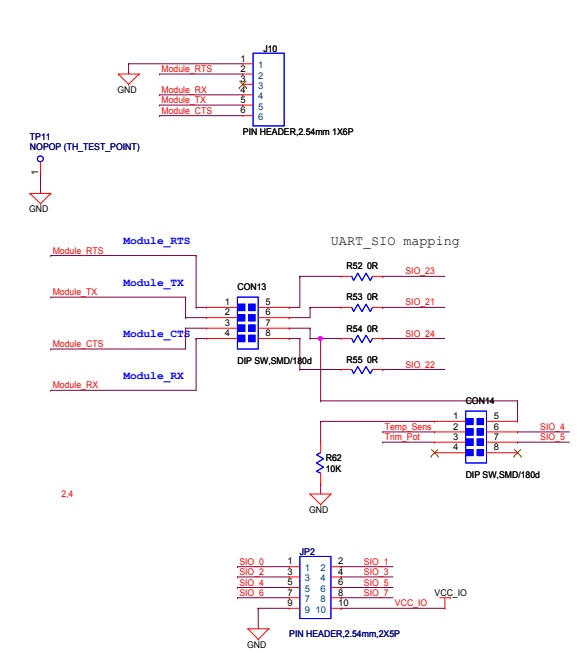
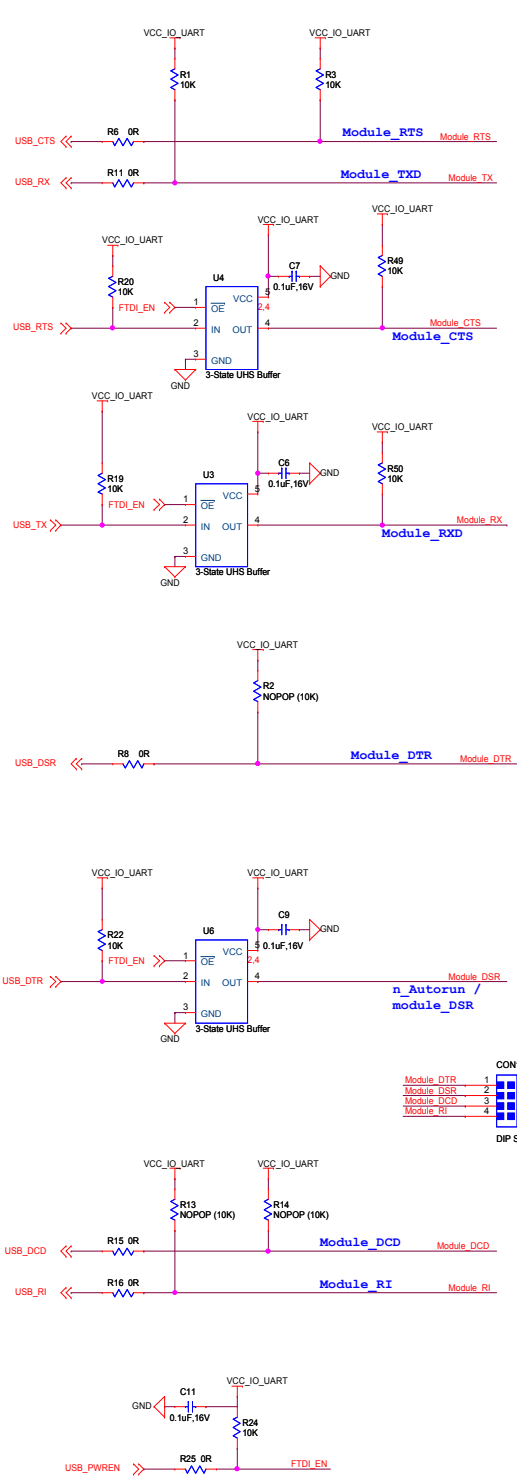


Use CN1 or J7 when:
A. interfacing to External UART into header connectors JP2, JP3, JP4, JP5.

VCC_IO is an output rail. Supplies external Header connectors.



Laird Technologies CPBU
 5F, No. 257, Dong Sec. 1, Guangming 6th Rd,
 Zhubei City, Hsinchu County 30264, Taiwan (R.O.C)

Project Name: DVK-BL600
 Schematic Name: DVK-BL600
 Drawing By: Kai_Wei

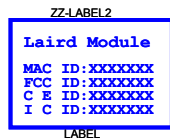
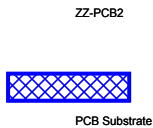
Date: 2014/08/21
 Sheet: 3 of 5
 Revision: V5


SE8089 3-Link Lite Cortex M-9 JTAG/SWD Emulator 10-pin connector. Top view.

DATE	REVISION NUMBER	INITIALS	Initial Release
	00 (EVT)	RK	
	01 (DVT)	RK	
	02 (PVT)	RK	
2014/04/30	03 (PVT V04)	Kai.Weii	Add J-Link Circuit
2014/06/23	04 (PVT V05)	Kai.Weii	U14 F3 from VCC_3V3 to VCC_IO_UART F5 from VCC_3V3 to VCC_IO_UART C10 from VCC_IO_UART to VCC_3V3 D8 from VCC_3V3 to VCC_IO_UART through a 100K RES J20 from VCC_3V3 to VCC_IO_UART J18 flip vertically
2014/08/21	05 (PVT V06)	Kai.Weii	Add R91 and connect to VCC_IO_UART Add R89 and connect to VCC_3V3 Add R90 and connect to VCC_IO_UART Let R88 connect to VCC_3V3 Let pin J3,K4 to NC Delete C50,C32

PCB design specification

1. Substrate: FR4 ROHS compliant, High TG 140 degree.
2. Solder mask, color=BLUE, Silkscreen color=WHITE
3. Surface finish to be Immersion Nickel/Gold (ENIG) with 1-2 u"
4. Start with 1/2 oz. copper on all layers.

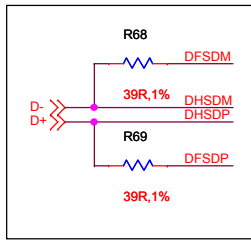


		Laird Technologies CPBU <small>5F, No. 257, Dong Sec. 1, Guangming Rd., Zhubei City, Hsinchu County, 30284, Taiwan (R.O.C.)</small>	
Project Name :	DVK-BL600	Schematic Name :	DVK-BL600
Date :	2014/08/21	Sheet :	2 of 5
		Drawing By :	Kai_Wei
		Revision :	V5

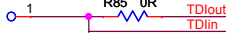
JLINK

Net	ATSAM3U2CA	BL600 module
SWDCLK	H4+K7+H8	Pin23
nRESET/SWDIO	H7+G7	Pin22

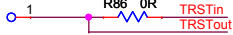
Close to MCU



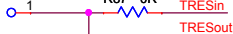
TP16 NOPOP (TH_TEST_POINT)



TP17 NOPOP (TH_TEST_POINT)



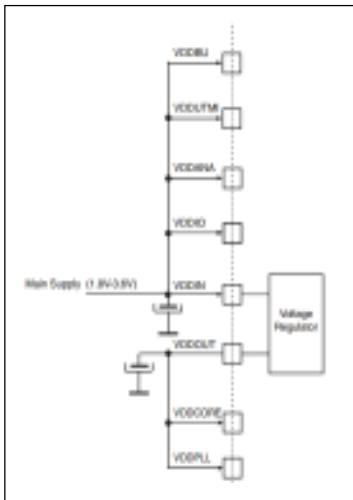
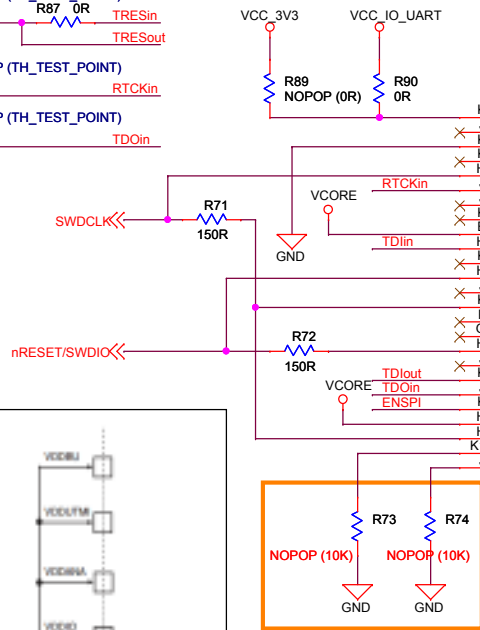
TP18 NOPOP (TH_TEST_POINT)



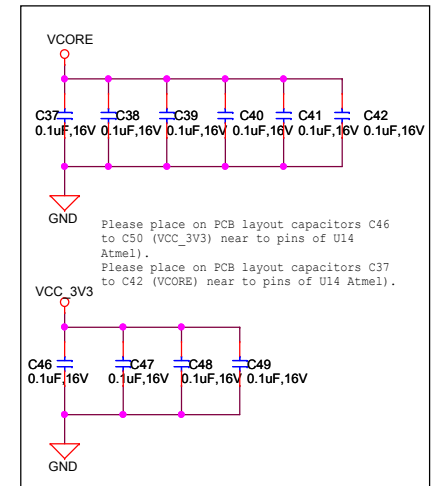
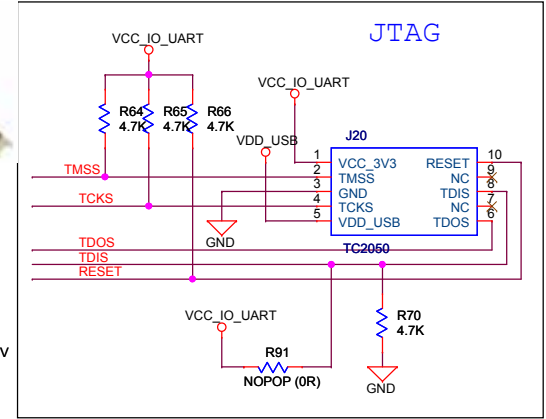
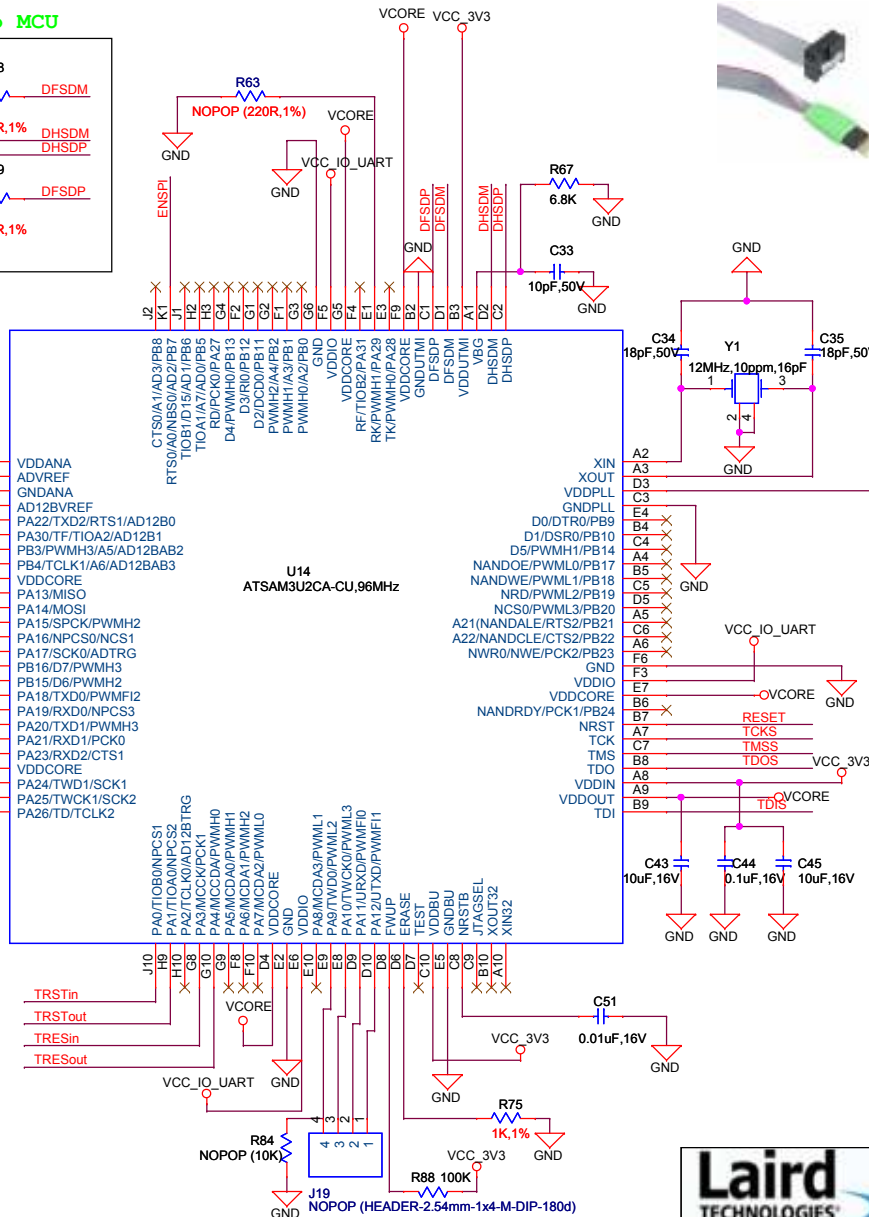
TP19 NOPOP (TH_TEST_POINT)



TP20 NOPOP (TH_TEST_POINT)



TO BL600



		Laird Technologies CPBU 5F, No. 257, Dong Sec. 1, Guangming 6th Rd, Zhubei City, Hsinchu County 30264, Taiwan (R.O.C)			
				Project Name :	Sehematic Name :
DVK-BL600		DVK-BL600		Kai_Wei	
Date :	2014/08/21	Sheet :	4 of 5	Revision :	V5